

# Extending a Brainiac Prover to Lambda-Free Higher-Order Logic (Technical Report)

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**Abstract.** Decades of work have gone into developing efficient proof calculi, data structures, algorithms, and heuristics for first-order automatic theorem proving. Higher-order provers lag behind in terms of efficiency. Instead of developing a new higher-order prover from the ground up, we propose to start with the state-of-the-art superposition-based prover E and gradually enrich it with higher-order features. We explain how to extend the prover’s data structures, algorithms, and heuristics to  $\lambda$ -free higher-order logic, a formalism that supports partial application and applied variables. Our extension outperforms the traditional encoding and appears promising as a stepping stone towards full higher-order logic.

## 1 Introduction

Superposition-based provers, such as E [42], SPASS [52], and Vampire [26], are among the most successful first-order reasoning systems. They serve as backends in various frameworks, including software verifiers (Why3 [22]), automatic higher-order theorem provers (Leo-III [44], Satallax [16]), and one-click “hammers” in proof assistants (HOLyHammer for HOL Light [24], Sledgehammer for Isabelle [34]). Decades of research have gone into refining calculi, devising efficient data structures and algorithms, and developing heuristics to guide proof search [43]. This work has mostly focused on first-order logic with equality, with or without arithmetic [21, 25, 36].

Research on higher-order automatic provers has resulted in systems such as LEO [9], LEO-II [11], and Leo-III [44], based on resolution and paramodulation, and Satallax [16], based on analytic tableaux and SAT solving. These provers feature a “cooperative” architecture, pioneered by LEO: They are full-fledged higher-order provers that regularly invoke an external first-order prover with a low time limit as a terminal procedure, in an attempt to finish the proof quickly using only first-order reasoning. However, the first-order backend will succeed

only if all the necessary higher-order reasoning has been performed, meaning that much of the first-order reasoning is carried out by the slower higher-order prover. As a result, this architecture leads to suboptimal performance on first-order problems and on problems with a large first-order component, such as those that often arise in interactive verification [46]. For example, at the 2017 installment of the CADE ATP System Competition (CASC) [49], Leo-III, using E as one of its backends, proved 652 out of 2000 first-order problems in the Sledgehammer division, compared with 1185 for E on its own and 1433 for Vampire.

To obtain better performance, we propose to start with a competitive first-order prover and extend it to full higher-order logic one feature at a time. Our goal is a *graceful* extension, so that the system behaves as before on first-order problems, performs mostly like a first-order prover on typical, mildly higher-order problems, and scales up to arbitrary higher-order problems, in keeping with the zero-overhead principle: *What you don't use, you don't pay for*.

As a stepping stone towards full higher-order logic, we initially restrict our focus to a higher-order logic without  $\lambda$ -expressions (Sect. 2). Compared with first-order logic, its distinguishing features are partial application and applied variables. This formalism is rich enough to express the recursive equations of higher-order combinators, such as the `map` operation on finite lists:

$$\text{map } f \text{ nil} \approx \text{nil} \qquad \text{map } f \text{ (cons } x \text{ xs)} \approx \text{cons } (f \ x) \text{ (map } f \ \text{xs)}$$

Our vehicle is E [39,42], a prover developed primarily by Schulz. It is written in C and offers good performance, with the emphasis on “brainiac” heuristics rather than raw speed. E regularly scores among the top systems at CASC, and usually is the strongest open source<sup>1</sup> prover in the relevant divisions. It also serves as a backend for competitive higher-order provers. We refer to our extended version of E as Ehoh. It corresponds to E version 2.3 configured with the option `-enable-ho`. A prototype of Ehoh is described in Vukmirović’s MSc thesis [51].

The three main challenges are generalizing the type and term representation (Sect. 3), the unification and matching algorithms (Sect. 4), and the indexing data structures (Sect. 5). We also adapted the inference rules (Sect. 6), the heuristics (Sect. 7), and the preprocessor (Sect. 8).

A novel aspect of our work is *prefix optimization*. Higher-order terms contain twice as many proper subterms as first-order terms; for example, the term `f (g a) b` contains not only the argument subterms `g a`, `a`, `b` but also the “prefix” subterms `f`, `f (g a)`, `g`. Many operations, including superposition and rewriting, require traversing all subterms of a term. Using prefix optimization, the prover traverses subterms recursively in a first-order fashion, considering all the prefixes of the current subterm together, at no significant additional cost. Our experiments (Sect. 9) show that Ehoh is effectively as fast as E on first-order problems and can also prove higher-order problems that do not require synthesizing  $\lambda$ -terms. As a next step, we plan to add support for  $\lambda$ -terms and higher-order unification.

<sup>1</sup> [http://www.lehre.dhbw-stuttgart.de/~sschulz/WORK/E\\_DOWNLOAD/V\\_2.3/](http://www.lehre.dhbw-stuttgart.de/~sschulz/WORK/E_DOWNLOAD/V_2.3/)

## 2 Logic

Our logic corresponds to the intensional  $\lambda$ -free higher-order logic ( $\lambda$ fHOL) described by Bentkamp, Blanchette, Cruanes, and Waldmann [8, Sect. 2]. Another possible name for this logic would be “applicative first-order logic.” Extensionality can be obtained by adding suitable axioms [8, Sect. 3.1].

A type is either an atomic type  $\iota$  or a function type  $\tau \rightarrow v$ , where  $\tau$  and  $v$  are themselves types. Terms, ranged over by  $s, t, u, v$ , are either *variables*  $x, y, z, \dots$ , (*function*) *symbols*  $a, b, c, d, f, g, \dots$  (often called “constants” in the higher-order literature), or binary applications of the form  $s t$ . Application associates to the left, whereas  $\rightarrow$  associates to the right. The typing rules are as for the simply typed  $\lambda$ -calculus: If  $s$  has type  $\tau \rightarrow v$  and  $t$  has type  $\tau$ , then  $s t$  has type  $v$ . A term’s *arity* is the number of extra arguments it can take; thus, if  $f$  has type  $\iota \rightarrow \iota \rightarrow \iota$  and  $a$  has type  $\iota$ , then  $f$  is binary,  $f a$  is unary, and  $f a a$  is nullary.

Terms have a unique “flattened” decomposition of the form  $\zeta s_1 \dots s_m$ , where  $\zeta$ , the *head*, is a variable  $x$  or symbol  $f$ , and  $s_1, \dots, s_m$ , the arguments, are arbitrary terms. We abbreviate tuples  $(a_1, \dots, a_m)$  to  $\overline{a_m}$  or  $\bar{a}$ ; abusing notation, we write  $\zeta \overline{s_m}$  for the curried application  $\zeta s_1 \dots s_m$ .

An equation  $s \approx t$  corresponds to an unordered pair of terms. A literal  $L$  is an equation  $s \approx t$ , where  $s$  and  $t$  must have the same type, or its negation, written  $s \not\approx t$ . Predicate symbols are encoded as terms of a distinguished Boolean type; for example,  $\text{even}(n)$  is encoded as  $\text{even}(n) \approx \text{true}$ . Clauses  $C, D$  are finite multisets of literals, interpreted disjunctively:  $L_1 \vee \dots \vee L_n$ . E and Ehoh clausify the input as a preprocessing step.

Substitutions  $\sigma$  are partial functions of finite domain from variables to terms, written  $\{x_1 \mapsto s_1, \dots, x_m \mapsto s_m\}$ , where each  $x_i$  has the same type as  $s_i$ . The notation  $\sigma[x \mapsto s]$  represents the substitution that maps  $x$  to  $s$  and that otherwise coincides with  $\sigma$ . Applying a substitution  $\sigma$  to a variable beyond  $\sigma$ ’s domain is the identity. Applying a substitution  $\sigma$  to a term  $t$  applies it homomorphically to  $t$ ’s variables. Composition  $(\sigma \circ \sigma')(t)$  is defined as  $\sigma(\sigma'(t))$ .

A well-known technique to support  $\lambda$ fHOL using first-order reasoning systems is to employ the *applicative encoding*. Following this scheme, every  $n$ -ary symbol is converted to a nullary symbol, and application is represented by a distinguished binary symbol  $@$ . For example, the  $\lambda$ fHOL term  $f(x a) b$  is encoded as the first-order term  $@(@ (f, @(x, a)), b)$ . However, this representation is not graceful; it clutters data structures and impacts proof search in subtle ways, leading to poorer performance, especially on large benchmarks. In our empirical evaluation, we find that for some prover modes, the applicative encoding incurs a 15% decrease in success rate (Sect. 9). For these and further reasons (Sect. 10), it is not an ideal basis for higher-order reasoning.

## 3 Types and Terms

The term representation is a fundamental question when building a theorem prover. Delicate changes to E’s term representation were needed to support

partial application and especially applied variables. In contrast, the introduction of a higher-order type system had a less dramatic impact on the prover’s code.

### 3.1 Types

For most of its history, E supported only untyped first-order logic. Cruanes implemented support for atomic types for E 2.0 [17, p. 117]. Symbols  $f$  are declared with a type signature:  $f : \tau_1 \times \dots \times \tau_m \rightarrow \tau$ . Atomic types are represented by integers in memory, leading to efficient type comparisons.

In  $\lambda$ HOL, a type signature consists of types  $\tau$ , in which the function type constructor  $\rightarrow$  can be nested—e.g.,  $(\iota \rightarrow \iota) \rightarrow \iota \rightarrow \iota$ . A natural way to represent such types is to mimic their recursive structures using tagged unions. However, this leads to memory fragmentation, and a simple operation such as querying the type of a function’s  $i$ th argument would require dereferencing  $i$  pointers. We prefer a flattened representation, in which a type  $\tau_1 \rightarrow \dots \rightarrow \tau_n \rightarrow \iota$  is represented by a single node labeled with  $\rightarrow$  and pointing to the array  $(\tau_1, \dots, \tau_n, \iota)$ . Applying  $k \leq n$  arguments to a function of the above type yields a term of type  $\tau_{k+1} \rightarrow \dots \rightarrow \tau_n \rightarrow \iota$ . In memory, this corresponds to skipping the first  $k$  array elements.

To speed up type comparisons, Ehoh stores all types in a shared bank and implements perfect sharing, ensuring that types that are structurally the same are represented by the same object in memory. Type equality can then be implemented as a pointer comparison, which is as efficient as E’s previous integer comparison.

Since higher-order types are structurally similar to first-order terms, it would have been possible to reuse E’s term data structure to store types. We experimented with this design choice but quickly abandoned the idea. E’s term module relies on a signature module to provide function code to function name mapping. For each symbol, the signature stores its type. If types are represented as terms, the signature module would depend on the term module, creating a cycle. There are workarounds to make such dependencies acceptable to the C compiler, but they are inelegant and would compromise the integrity of E’s source code.

### 3.2 Terms

In E, terms are represented as perfectly shared directed acyclic graphs [29]. Each node, or *cell*, contains 11 fields, including `f_code`, an integer that identifies the term’s head symbol (if  $\geq 0$ ) or variable (if  $< 0$ ); `arity`, an integer corresponding to the number of arguments passed to the head symbol; `args`, an array of size `arity` consisting of pointers to argument terms; and `binding`, which possibly stores a substitution for a variable (if `f_code`  $< 0$ ) used for unification and matching.

In first-order logic, the arity of variables is always 0, and the arity of a symbol  $f$  is given by its type signature. In higher-order logic, variables may have function type and be applied, and symbols can be applied to fewer arguments than specified by their type signatures. A natural representation of  $\lambda$ HOL terms as tagged unions would distinguish between variables  $x$ , symbols  $f$ , and binary applications

*s t*. However, this scheme suffers from memory fragmentation and linear-time access, as with the representation of types, affecting performance on purely or mostly first-order problems. Instead, we propose a flattened representation, as a generalization of E’s existing data structures: Allow arguments to variables, and for symbols let **arity** be the number of actual arguments, as opposed to the declared arity, and rename the field **num\_args**. This approach parallels our representation of types.

A side effect of the flattened representation is that prefix subterms are not shared. For example, the terms  $f\ a$  and  $f\ a\ b$  correspond to the flattened cells  $f(a)$  and  $f(a, b)$ . The argument subterm  $a$  is shared, but not the prefix  $f\ a$ . Similarly,  $x$  and  $x\ b$  are represented by two distinct cells,  $x()$  and  $x(b)$ , and there is no connection between the two occurrences of  $x$ . In particular, despite perfect sharing, their **binding** fields are unconnected, leading to inconsistencies.

A potential solution would be to systematically traverse a clause and set the **binding** fields of all cells of the form  $x(\bar{s})$  whenever a variable  $x$  is bound, but this would be inefficient and inelegant. Instead, we implemented a hybrid approach: Variables are applied by an explicit application operator  $@$ , to ensure that they are always perfectly shared. Thus,  $x\ b\ c$  is represented by the cell  $@(x, b, c)$ , where  $x$  is a shared subcell. This complicates the representation somewhat (and led to subtle bugs during development), but it is graceful, since variables never occur applied in first-order terms. The main drawback of this technique is that some normalization is necessary after substitution: Whenever a variable is instantiated by a term with a symbol head, the  $@$  symbol must be eliminated. Applying the substitution  $\{x \mapsto f\ a\}$  to the cell  $@(x, b, c)$  must produce the cell  $f(a, b, c)$  and not  $@(f(a), b, c)$ , for consistency with other occurrences of  $f\ a\ b\ c$ .

There is one more complication related to the **binding** field. In E, it is easy and useful to traverse a term as if a substitution has been applied, by following all set **binding** fields. In Ehoh, this is not enough, because cells must also be normalized. To avoid repeatedly creating the same normalized cells, we introduced a **binding\_cache** field that connects a  $@(x, \bar{s})$  cell with its substitution. However, this cache can easily become stale when the **binding** pointer is updated. To detect this situation, we store  $x$ ’s **binding** value in the  $@(x, \bar{s})$  cell’s **binding** field (which is otherwise unused). To find out whether the cache is valid, it suffices to check that the **binding** fields of  $x$  and  $@(x, \bar{s})$  are equal.

### 3.3 Term Orders

Superposition provers rely on term orders to prune the search space. To ensure completeness of the underlying calculus, the order must be a simplification order that can be extended to a simplification order that is total on variable-free terms. The Knuth–Bendix order (KBO) and the lexicographic path order (LPO) meet this criterion. KBO is generally regarded as the more robust and efficient option for superposition, but LPO can be used to prove some problems more quickly. E implements both. In earlier work, Blanchette and colleagues have shown that only KBO can be generalized gracefully while preserving all the necessary properties for superposition [6, 14]. For this reason, we focus on KBO.

E implements the linear-time algorithm for KBO described by Löchner [28], which relies on the tupling method to store intermediate results, avoiding repeated computations. It is straightforward to generalize the algorithm to compute the graceful  $\lambda$ FHOL version of KBO [6]. The main difference is that when comparing two terms  $f \overline{s_m}$  and  $f \overline{t_n}$ , because of partial application we may now have  $m \neq n$ ; this required changing the implementation to perform a length-lexicographic comparison of the tuples  $\overline{s_m}$  and  $\overline{t_n}$ .

## 4 Unification and Matching

Syntactic unification of  $\lambda$ FHOL terms has a definite first-order flavor. It is decidable, and most general unifiers (MGUs) are unique up to variable renaming. For example, the unification constraint  $f(y \ a) \stackrel{?}{=} f(a)$  has the MGU  $\{y \mapsto a\}$ , whereas in full higher-order logic it would admit infinitely many independent solutions of the form  $\{y \mapsto \lambda x. f(f(\dots(f x)\dots))\}$ . Matching is a special case of unification where only the variables on the left-hand side can be instantiated.

An easy but inefficient way to implement unification and matching for  $\lambda$ FHOL is to apply the applicative encoding (Sect. 1), perform first-order unification or matching, and decode the resulting substitution. However, the applicative encoding introduces an undesirable overhead. Instead, we propose to generalize the first-order unification and matching procedures to operate directly on  $\lambda$ FHOL terms.

### 4.1 Unification

We present our unification procedure as a nondeterministic transition system, generalizing Baader and Nipkow [4]. A unification problem consists of a finite set  $S$  of unification constraints  $s_i \stackrel{?}{=} t_i$ , where  $s_i$  and  $t_i$  are of the same type. A problem is in *solved form* if it has the form  $\{x_1 \stackrel{?}{=} t_1, \dots, x_n \stackrel{?}{=} t_n\}$ , where the  $x_i$ 's are distinct and do not occur in the  $t_j$ 's. The corresponding unifier is  $\{x_1 \mapsto t_1, \dots, x_n \mapsto t_n\}$ . The transition rules attempt to bring the input constraints into solved form. They can be applied in any order and eventually reach a normal form, which is either an idempotent MGU expressed in solved form or the special value  $\perp$ , denoting unsatisfiability of the constraints.

The first group of rules—the *positive* rules—consists of operations that focus on a single constraint and replace it with a new (possibly empty) set of constraints:

Delete	$\{t \stackrel{?}{=} t\} \uplus S \Longrightarrow S$
Decompose	$\{f \overline{s_m} \stackrel{?}{=} f \overline{t_m}\} \uplus S \Longrightarrow S \cup \{s_1 \stackrel{?}{=} t_1, \dots, s_m \stackrel{?}{=} t_m\}$
DecomposeX	$\{x \overline{s_m} \stackrel{?}{=} u \overline{t_m}\} \uplus S \Longrightarrow S \cup \{x \stackrel{?}{=} u, s_1 \stackrel{?}{=} t_1, \dots, s_m \stackrel{?}{=} t_m\}$ if $x$ and $u$ have the same type and $m > 0$
Orient	$\{f \overline{s} \stackrel{?}{=} x \overline{t}\} \uplus S \Longrightarrow S \cup \{x \overline{t} \stackrel{?}{=} f \overline{s}\}$
OrientXY	$\{x \overline{s_m} \stackrel{?}{=} y \overline{t_n}\} \uplus S \Longrightarrow S \cup \{y \overline{t_n} \stackrel{?}{=} x \overline{s_m}\}$ if $m > n$
Eliminate	$\{x \stackrel{?}{=} t\} \uplus S \Longrightarrow \{x \stackrel{?}{=} t\} \cup \{x \mapsto t\}(S)$ if $x \in \text{Var}(S) \setminus \text{Var}(t)$

The Delete, Decompose, and Eliminate rules are essentially as for first-order terms. The Orient rule is generalized to allow applied variables and complemented by a new OrientXY rule. DecomposeX, also a new rule, can be seen as a variant of Decompose that analyzes applied variables; the term  $u$  may be an application.

The rules belonging to the second group—the *negative* rules—detect unsolvable constraints:

$$\begin{aligned}
\text{Clash} & \quad \{f \bar{s} \stackrel{?}{=} g \bar{t}\} \uplus S \Longrightarrow \perp \quad \text{if } f \neq g \\
\text{ClashTypeX} & \quad \{x \bar{s}_m \stackrel{?}{=} u \bar{t}_m\} \uplus S \Longrightarrow \perp \quad \text{if } x \text{ and } u \text{ have different types} \\
\text{ClashLenXF} & \quad \{x \bar{s}_m \stackrel{?}{=} f \bar{t}_n\} \uplus S \Longrightarrow \perp \quad \text{if } m > n \\
\text{OccursCheck} & \quad \{x \stackrel{?}{=} t\} \uplus S \Longrightarrow \perp \quad \text{if } x \in \mathcal{V}ar(t) \text{ and } x \neq t
\end{aligned}$$

The Clash and OccursCheck rules are essentially as in Baader and Nipkow. The ClashTypeX and ClashLenXF rules are variants of Clash for applied variables.

The derivations below demonstrate the computation of MGUs for the unification problems  $\{f(y a) \stackrel{?}{=} y(f a)\}$  and  $\{x(z b c) \stackrel{?}{=} g a(y c)\}$ :

$$\begin{array}{ll}
\Longrightarrow_{\text{Orient}} & \{f(y a) \stackrel{?}{=} y(f a)\} \\
\Longrightarrow_{\text{DecomposeX}} & \{y \stackrel{?}{=} f, f a \stackrel{?}{=} y a\} \\
\Longrightarrow_{\text{Eliminate}} & \{y \stackrel{?}{=} f, f a \stackrel{?}{=} f a\} \\
\Longrightarrow_{\text{Delete}} & \{y \stackrel{?}{=} f\}
\end{array}
\qquad
\begin{array}{ll}
\Longrightarrow_{\text{DecomposeX}} & \{x(z b c) \stackrel{?}{=} g a(y c)\} \\
\Longrightarrow_{\text{OrientXY}} & \{x \stackrel{?}{=} g a, z b c \stackrel{?}{=} y c\} \\
\Longrightarrow_{\text{DecomposeX}} & \{x \stackrel{?}{=} g a, y \stackrel{?}{=} z b, c \stackrel{?}{=} c\} \\
\Longrightarrow_{\text{Delete}} & \{x \stackrel{?}{=} g a, y \stackrel{?}{=} z b\}
\end{array}$$

E stores open constraints in a double-ended queue. Constraints are processed from the front. New constraints are added at the front if they involve complex terms that can be dealt with swiftly by Decompose or Clash, or to the back if one side is a variable. This delays instantiation of variables (with a possible increase in term size) and allows E to detect structural clashes early.

During proof search, E repeatedly needs to test a term  $s$  for unifiability not only with some other term  $t$  but also with  $t$ 's subterms. Prefix optimization speeds up this test: The subterms of  $t$  are traversed in a first-order fashion; for each such subterm  $\zeta \bar{t}_n$ , at most one prefix  $\zeta \bar{t}_k$ , with  $k \leq n$ , is possibly unifiable with  $s$ , by virtue of their having the same arity. For first-order problems, we can only have  $k = n$ , since all functions are fully applied. Using this technique, Ehoh is virtually as efficient as E on first-order terms.

The transition system introduced above always terminates with a correct answer. Our proofs follow the lines of Baader and Nipkow.

In the following, the metavariable  $\mathcal{R}$  is used to range over constraint sets  $S$  and the special value  $\perp$ . The set of all unifiers of  $S$  is denoted by  $\mathcal{U}(S)$ . Note that  $\mathcal{U}(S \cup S') = \mathcal{U}(S) \cap \mathcal{U}(S')$ . By convention, we let  $\mathcal{U}(\perp) = \emptyset$ . The notation  $S \Longrightarrow^! S'$  indicates that  $S \Longrightarrow^* S'$  and  $S'$  is a normal form (i.e., there exists no  $S''$  such that  $S' \Longrightarrow S''$ ). A variable  $x$  is *solved* in  $S$  if it occurs exactly once in  $S$ , in a constraint of the form  $x \stackrel{?}{=} t$ .

**Lemma 1.** *If  $S \Longrightarrow \mathcal{R}$ , then  $\mathcal{U}(S) = \mathcal{U}(\mathcal{R})$ .*

*Proof.* The rules Delete, Decompose, Orient, and Eliminate are essentially as in Baader and Nipkow. The same arguments carry over to  $\lambda\text{FHOL}$ . OrientXY trivially preserves unifiers. For DecomposeX, the core of the argument is as follows:

$$\begin{aligned}
& \sigma \in \mathcal{U}(\{x \overline{s_m} \stackrel{?}{=} u \overline{t_m}\}) \\
& \text{iff } \sigma(x \overline{s_m}) = \sigma(u \overline{t_m}) \\
& \text{iff } \sigma(x) \sigma(s_1) \dots \sigma(s_m) = \sigma(u) \sigma(t_1) \dots \sigma(t_m) \\
& \text{iff } \sigma(x) = \sigma(u), \sigma(s_1) = \sigma(t_1), \dots, \text{ and } \sigma(s_m) = \sigma(t_m) \\
& \text{iff } \sigma \in \mathcal{U}(\{x \stackrel{?}{=} u, s_1 \stackrel{?}{=} t_1, \dots, s_m \stackrel{?}{=} t_m\})
\end{aligned}$$

The proof of the problem's unsolvability if Clash or OccursCheck are applicable carries over from Baader and Nipkow. For ClashTypeX, the argument is that  $\sigma(x \overline{s_m}) = \sigma(u \overline{t_m})$  is possible only if  $\sigma(x) = \sigma(u)$ , which requires  $x$  and  $u$  to be of the same type. Similarly, for ClashLenXF, if  $\sigma(x \overline{s_m}) = \sigma(f \overline{t_n})$  with  $m > n$ , we must have  $\sigma(x \overline{s_{m-n}}) = \sigma(x) \sigma(s_1) \dots \sigma(s_{m-n}) = f$ , which is impossible.  $\square$

**Lemma 2.** *If  $S$  is a normal form, then  $S$  is in solved form.*

*Proof.* Consider an arbitrary unification constraint  $s \stackrel{?}{=} t \in S$ . We show that in all but one cases, a rule is applicable, contradicting the hypothesis that  $S$  is a normal form. In the remaining case,  $s$  is a solved variable in  $S$ .

CASE  $s = x$ :

- SUBCASE  $t = x$ : Delete is applicable.
- SUBCASE  $t \neq x$  and  $x \in \text{Var}(t)$ : OccursCheck is applicable.
- SUBCASE  $t \neq x$ ,  $x \notin \text{Var}(t)$ , and  $x \in \text{Var}(S \setminus \{s \stackrel{?}{=} t\})$ : Eliminate is applicable.
- SUBCASE  $t \neq x$ ,  $x \notin \text{Var}(t)$ , and  $x \notin \text{Var}(S \setminus \{s \stackrel{?}{=} t\})$ : The variable  $x$  is solved in  $S$ .

CASE  $s = x \overline{s_m}$  for  $m > 0$ :

- SUBCASE  $t = \eta \overline{t_n}$  for  $n \geq m$ : DecomposeX or ClashTypeX is applicable, depending on whether  $x$  and  $\eta \overline{t_{n-m}}$  have the same type.
- SUBCASE  $t = y \overline{t_n}$  for  $n < m$ : OrientXY is applicable.
- SUBCASE  $t = f \overline{t_n}$  for  $n < m$ : ClashLenXF is applicable.

CASE  $s = f \overline{s_m}$ :

- SUBCASE  $t = x \overline{t_n}$ : Orient is applicable.
- SUBCASE  $t = f \overline{t_n}$ : Due to well-typedness,  $m = n$ . Decompose is applicable.
- SUBCASE  $t = g \overline{t_n}$ : Clash is applicable.

Since each constraint is of the form  $x \stackrel{?}{=} t$  where  $x$  is solved in  $S$ , the problem  $S$  is in solved form.  $\square$

**Lemma 3.** *If the constraint set  $S$  is in solved form, then the associated substitution is an idempotent MGU of  $S$ .*



*Proof.* This lemma corresponds to Lemma 4.6.3 of Baader and Nipkow. Their proof carries over to  $\lambda$ FHOL.  $\square$

**Theorem 4 (Partial Correctness).** *If  $S \Longrightarrow^! \perp$ , then  $S$  has no solutions. If  $S \Longrightarrow^! S'$ , then  $S'$  is in solved form and the associated substitution is an idempotent MGU of  $S$ .*

*Proof.* The first part follows from Lemma 1 applied transitively. The second part follows from Lemma 1 applied transitively and Lemmas 2 and 3.  $\square$

**Theorem 5 (Termination).** *The relation  $\Longrightarrow$  is well founded.*

*Proof.* First, we define the auxiliary notion of weight of a term:

$$\mathcal{W}(\zeta \overline{s_m}) = m + 1 + \sum_{i=1}^m \mathcal{W}(s_i)$$

Well-foundedness is proved by exhibiting a measure function from constraint sets to quadruples of natural numbers  $(n_1, n_2, n_3, n_4)$ , where

- $n_1$  is the number of unsolved variables in  $S$ ;
- $n_2$  is the sum of all term weights:  $\sum_{s \stackrel{?}{=} t \in S} \mathcal{W}(s) + \mathcal{W}(t)$ ;
- $n_3$  is the number of right-hand side variable heads:  $|\{s \stackrel{?}{=} x \bar{t} \in S\}|$ ;
- $n_4$  is the number of arguments passed to left-hand side variable heads:  $\sum_{x \overline{s_m} \stackrel{?}{=} t \in S} m$ .

The following table shows that the application of each positive rule lexicographically decreases the quadruple:

	$n_1$	$n_2$	$n_3$	$n_4$
Delete	$\geq$	$>$		
Decompose	$\geq$	$>$		
DecomposeX	$\geq$	$>$		
Orient	$\geq$	$=$	$>$	
OrientXY	$\geq$	$=$	$=$	$>$
Eliminate	$>$			

Eliminate, by solving one variable, decreases  $n_1$ . Delete removes a constraint from  $S$ , decreasing the weight. Decompose reduces the weight by 2. Prefix reduces the set weight by at least  $m > 0$ , thanks to the presence of  $m$  in the definition of  $\mathcal{W}$ . The negative rules, which produce the special value  $\perp$ , cannot contribute to an infinite  $\Longrightarrow$  chain.

For example, the quadruples corresponding to the derivation starting from  $\{x(z \text{ b } c) \stackrel{?}{=} g \text{ a } (y \text{ c})\}$  given above are  $(3, 14, 0, 1) > (2, 12, 1, 2) > (2, 12, 1, 1) > (1, 10, 1, 0) > (1, 8, 1, 0)$ .  $\square$

A unification algorithm for  $\lambda$ FHOL can be derived from the above transition system, by committing to a strategy for applying the rules. This algorithm closely follows the Ehoh implementation, abstracting away from complications such as prefix optimization. We assume a flattened representation of terms; as in Ehoh,

each variable stores the term it is bound to in *binding* field (Sect. 3). We also rely on a APPLYSUBST function, which applies the *binding* to the top-level variable. The algorithm assumes that the terms to be unified have the same type. The pseudocode is as follows:

```

function SWAPNEEDED(Term s, Term t) is
  return t.head().isVar()
     $\wedge (\neg s.head().isVar() \vee s.num\_args > t.num\_args)$ 

function DEREf(Term s) is
  while s.head().isVar()  $\wedge$  s.head().binding  $\neq$  Null do
    s  $\leftarrow$  APPLYSUBST(s, s.head().binding)
  return s

function GOBBLEPREFIX(Term x, Term t) is
  res  $\leftarrow$  Null
  if x.type().args is suffix of t.head().type().args then
    pref_len  $\leftarrow$  t.head().type().arity - x.type().arity
    if pref_len  $\leq$  t.num_args then
      res  $\leftarrow$  TERM(t.head(), t.args[1 .. pref_len])
  return res

function UNIFY(Term s, Term t) is
  constraints  $\leftarrow$  DOUBLEENDEDQUEUE()
  constraints.prepend(s)
  constraints.prepend(t)
  while  $\neg$  constraints.isEmpty() do
    t  $\leftarrow$  DEREf(constraints.dequeue())
    s  $\leftarrow$  DEREf(constraints.dequeue())
    if s  $\neq$  t then
      if SWAPNEEDED(s, t) then
        (t, s)  $\leftarrow$  (s, t)
      if s.head().isVar() then
        x  $\leftarrow$  s.head()
        prefix  $\leftarrow$  GOBBLEPREFIX(x, t)
        if prefix  $\neq$  Null then
          start_idx  $\leftarrow$  prefix.num_args + 1
          if x occurs in prefix then
            return False
          else
            x.binding  $\leftarrow$  prefix
          else
            return False
        else if s.head() = t.head() then
          start_idx  $\leftarrow$  1
        else
          return False

```

```

for  $i \leftarrow start\_idx$  to  $t.num\_args$  do
   $s\_arg \leftarrow s.args[i - start\_idx + 1]$ 
   $t\_arg \leftarrow t.args[i]$ 
  if  $s\_arg.head().isVar() \vee t\_arg.head().isVar()$  then
     $constraints.append(t\_arg)$ 
     $constraints.append(s\_arg)$ 
  else
     $constraints.prepend(s\_arg)$ 
     $constraints.prepend(t\_arg)$ 
return True

```

The UNIFY function encompasses all the transition rules given above. The  $s \neq t$  test in the body of UNIFY’s **while** loop corresponds to Delete. The exchange triggered by the SWAPNEEDED test corresponds to Orient or OrientXY. The GOBBLEPREFIX function determines whether Prefix is applicable; if so, it finds the appropriate prefix subterm. *Null* is returned if either ClashTypeX or ClashLenXF are applicable, resulting in a failure of UNIFY. A variable is bound to a term only if it does not occur in the term, reflecting OccursCheck. The failure that arises if  $s$  and  $t$  have different head symbols corresponds to Clash. To discover such failures as early as possible, constraints with nonvariable heads are put in the front of the queue (in the **for** loop at the end of UNIFY). Finally, Eliminate is applied implicitly—by following variable bindings in Deref, we incrementally apply the substitution we build.

## 4.2 Matching

Given two terms  $s$  and  $t$ , the matching problem consists of finding a substitution  $\sigma$  such that  $\sigma(s) = t$ . We then write that “ $t$  is an instance of  $s$ ” or “ $s$  generalizes  $t$ ” and call  $\sigma$  *generalizing substitution*. We are interested in most general generalizations (MGGs). Matching can be reduced to unification by treating variables in  $t$  as constants [4], but E implements matching as a separate, optimized procedure.

Matching can be specified abstractly as a transition system on constraints  $s_i \lesssim^? t_i$  consisting of the unification rules Decompose, Prefix, Clash, ClashTypeX, ClashLenXF (with  $\lesssim^?$  instead of  $\stackrel{?}{=}$ ) and augmented with

```

Double       $\{x \lesssim^? t, x \lesssim^? t'\} \uplus S \implies \perp$    if  $t \neq t'$ 
ClashLenXY   $\{x \overline{s}_m \lesssim^? y \overline{t}_n\} \uplus S \implies \perp$    if  $x \neq y$  and  $m > n$ 
ClashFX      $\{f \overline{s} \lesssim^? x \overline{t}\} \uplus S \implies \perp$ 

```

Interestingly, the rule Delete is not sound for matching. Suppose that we are given problem  $\{x \lesssim^? x, x \lesssim^? g x\}$ . Application of Delete to the first constraint would yield end state  $\{x \lesssim^? g x\}$ , even though the original problem has no solution.

Matching rules are both well founded and complete. Both properties are proven analogously to unification rules.

**Theorem 6 (Partial Correctness).** *If  $S \implies^! \perp$ , then  $S$  has no solutions. If  $S \implies^! S'$ , then  $S'$  is solved form and the associated substitution is the MGG of  $S$ .*

**Theorem 7 (Termination).** *The relation  $\implies$  is well founded.*

The pseudocode for matching closely follows that given for unification:

```

function MATCH(Term s, Term t) is
  constraints  $\leftarrow$  STACK()
  constraints.push(s)
  constraints.push(t)
  while  $\neg$  constraints.isEmpty() do
    t  $\leftarrow$  constraints.pop()
    s  $\leftarrow$  constraints.pop()
    if s.head.isVar() then
      x  $\leftarrow$  s.head()
      prefix  $\leftarrow$  GOBBLEPREFIX(x, t)
      if prefix  $\neq$  Null  $\wedge$  (x.binding = Null  $\vee$  x.binding = prefix) then
        start_idx  $\leftarrow$  prefix.num_args + 1
        x.binding  $\leftarrow$  prefix
      else
        return False
    else if s.head() = t.head() then
      start_idx  $\leftarrow$  1
    else
      return False
    for i  $\leftarrow$  start_idx to t.num_args do
      constraints.push(s.args[i - start_idx + 1])
      constraints.push(t.args[i])
  return True

```

The rules that are common to unification and matching are applied in the same way. The absence of the SWAPNEEDED test reflects the absence of Orient and OrientXY rules. Similarly, the absence of Deref calls reflects the absence of Eliminate. Binding a variable to a term is now done without checking for occurrences of the former in the latter, effectively disabling OccursCheck. The algorithm checks whether a variable is already bound before binding it, corresponding to the new Double rule. Finally, GOBBLEPREFIX will return *Null* if ClashFX or ClashLenXY applies.

## 5 Indexing Data Structures

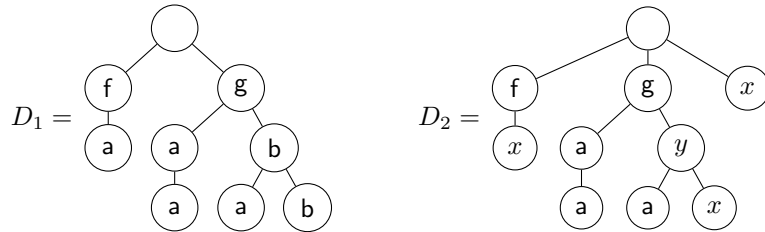
Superposition provers like E work by saturation. Their main loop heuristically selects a clause and searches for potential inference partners among a possibly large set of other clauses. Mechanisms such as simplification and subsumption also require locating terms in a large clause set. For example, when E derives a new equation  $s \approx t$ , if  $s$  is larger than  $t$  according to the term order, it will rewrite all instances  $\sigma(s)$  of  $s$  to  $\sigma(t)$  in existing clauses.

To avoid iterating over all terms (including subterms) in large clause sets, superposition provers store the potential inference partners in indexing data structures. A term index stores a set of terms  $S$ . Given a *query term*  $t$ , a query returns all terms  $s \in S$  that satisfy a given *retrieval condition*:  $\sigma(s) = \sigma(t)$  ( $s$  and  $t$  are unifiable),  $\sigma(s) = t$  ( $s$  generalizes  $t$ ), or  $s = \sigma(t)$  ( $s$  is an instance of  $t$ ), for some substitution  $\sigma$ . *Perfect* indices return exactly the subset of terms satisfying the retrieval condition. In contrast, *imperfect* indices return a superset of eligible terms, and the retrieval condition needs to be checked for each candidate.

E relies on two term indexing data structures, perfect discrimination trees [31] and fingerprint indices [40], that needed to be generalized to  $\lambda$ FHOL. It also uses feature vector indices [41] to speed up clause subsumption and related techniques, but these require no changes to work with  $\lambda$ FHOL clauses.

### 5.1 Perfect Discrimination Trees

Discrimination trees [31] are tries in which every node is labeled with a symbol or a variable. A path from the root to a leaf node corresponds to a “serialized term”—a term expressed without parentheses and commas. Consider the following discrimination trees:



Assuming  $a, b, x, y : \iota$ ,  $f : \iota \rightarrow \iota$ , and  $g : \iota^2 \rightarrow \iota$ , the trees  $D_1$  and  $D_2$  represent the term sets  $\{f(a), g(a, a), g(b, a), g(b, b)\}$  and  $\{f(x), g(a, a), g(y, a), g(y, x), x\}$ .

E uses perfect discrimination trees for finding generalizations of query terms. For example, if the query term is  $g(a, a)$ , it would follow the path  $g.a.a$  in the tree  $D_1$  and return  $\{g(a, a)\}$ . For  $D_2$ , it would also explore paths labeled with variables, binding them as it proceeds, and return  $\{g(a, a), g(y, a), g(y, x), x\}$ .

The data structure relies on the observation that serializing is unambiguous: Assuming that symbols and variables are declared with unique types, distinct terms always give rise to distinct serialized terms. Conveniently, this property also holds for  $\lambda$ FHOL terms. Assume that two distinct  $\lambda$ FHOL terms yield the same serialization. Clearly, they must disagree on parentheses; one will have the subterm  $s t u$  where the other has  $s (t u)$ . However, these two subterms cannot both be well typed; if  $t$  has the right type to be passed as argument to  $s$ , then  $t u$  must have the wrong type.

When generalizing the data structure to  $\lambda$ FHOL, we face a slight complication due to partial application. First-order terms can only be stored in leaf nodes, but in Ehol we must also be able to represent partially applied terms, such as  $f$ ,  $g$ , or  $g a$  (assuming, as above, that  $f$  is unary and  $g$  is binary). Conceptually,

this can be solved by storing a Boolean on each node indicating whether it is an accepting state. In the implementation, the change is more subtle, because several parts of E’s code implicitly assume that only leaf nodes are accepting.

The main difficulty specific to  $\lambda$ fHOL concerns applied variables. To enumerate all generalizing terms, E needs to backtrack from child to parent nodes. To achieve this, it relies on two stacks that store subterms of the query term:

- **term\_stack** stores the terms that must be matched in turn against the current subtree;
- **term\_proc** stores, for each node from the root to the current subtree, the corresponding processed term, including any arguments yet to be matched.

The matching procedure starts at the root with an empty substitution  $\sigma$ . Initially, **term\_stack** contains the query term, and **term\_proc** is empty. The procedure advances by moving to a suitable child node:

- If the node is labeled with a symbol  $f$  and the top item  $t$  of **term\_stack** is of the form  $f(\bar{t}_n)$ , replace  $t$  by  $n$  new items  $t_1, \dots, t_n$ , and push  $t$  onto **term\_proc**.
- If the node is labeled with a variable  $x$ , there are two subcases. If  $x$  is already bound, check that  $\sigma(x) = t$ ; otherwise, extend  $\sigma$  so that  $\sigma(x) = t$ . Next, pop a term  $t$  from **term\_stack** and push it onto **term\_proc**.

The goal is to reach an accepting node. If the query term and all the terms stored in the tree are first-order, **term\_stack** will then be empty, and the entire query term will have been matched.

Backtracking works in reverse: Pop a term  $t$  from **term\_proc**; if the current node is labeled with an  $n$ -ary symbol, discard **term\_stack**’s topmost  $n$  items; finally, push  $t$  onto **term\_stack**. Variable bindings must also be undone.

As an example, looking up  $g(b, a)$  in the tree  $D_1$  would result in the following succession of stack states, starting from the root  $\epsilon$  along the path  $g.b.a$ :

	$\epsilon$	$g$	$g.b$	$g.b.a$
<b>term_stack:</b>	$[g(b, a)]$	$[b, a]$	$[a]$	$[]$
<b>term_proc:</b>	$[]$	$[g(b, a)]$	$[b, g(b, a)]$	$[a, b, g(b, a)]$

(The notation  $[a_1, \dots, a_n]$  represents the  $n$ -item stack with  $a_1$  on top.) Backtracking amounts to moving leftwards: When backtracking from the node  $g$  to the root, we pop  $g(b, a)$  from **term\_proc**, we discard two items from **term\_stack**, and we push  $g(b, a)$  onto **term\_stack**.

To adapt the procedure to  $\lambda$ fHOL, the key idea is that an applied variable is not very different from an applied symbol. A node labeled with an  $n$ -ary symbol or variable  $\zeta$  matches a prefix  $t'$  of the  $k$ -ary term  $t$  popped from **term\_stack** and leaves  $n - k$  arguments  $\bar{u}$  to be pushed back, with  $t = t' \bar{u}$ . If  $\zeta$  is a variable, it must be bound to the prefix  $t'$ . Backtracking works analogously: Given the arity  $n$  of the node label  $\zeta$  and the arity  $k$  of the term  $t$  popped from **term\_proc**, we discard the topmost  $n - k$  items  $\bar{u}$  from **term\_proc**.

To illustrate the procedure, we consider the tree  $D_2$  but change  $y$ 's type to  $\iota \rightarrow \iota$ . This tree represents the set  $\{f\ x, g\ a\ a, g\ (y\ a), g\ (y\ x), x\}$ . Let  $g\ (g\ a\ b)$  be the query term. We have the following sequence of substitutions and stacks:

	$\epsilon$	$g$	$g.y$	$g.y.x$
$\sigma$ :	$\emptyset$	$\emptyset$	$\{y \mapsto g\ a\}$	$\{y \mapsto g\ a, x \mapsto b\}$
<b>term_stack</b> :	$[g\ (g\ a\ b)]$	$[g\ a\ b]$	$[b]$	$[]$
<b>term_proc</b> :	$[]$	$[g\ (g\ a\ b)]$	$[g\ a\ b, g\ (g\ a\ b)]$	$[b, g\ a\ b, g\ (g\ a\ b)]$

When backtracking from  $g.y$  to  $g$ , by comparing  $y$ 's arity of  $n = 1$  with  $g\ a\ b$ 's arity of  $k = 0$ , we determine that one item must be discarded from **term\_stack**. Finally, to avoid traversing twice as many subterms as in the first-order case, we can optimize prefixes: Given a query term  $\zeta\ \bar{t}_n$ , we can also match prefixes  $\zeta\ \bar{t}_k$ , where  $k < n$ , by allowing **term\_stack** to be nonempty at the end. If accepting nodes correspond to legal prefixes, the stack will then contain exactly the remaining arguments:  $[t_{k+1}, \dots, t_n]$ .

Similarly to unification and matching, we present finding generalizations in a perfect discrimination tree as a transition system. States are quadruples  $\mathcal{Q} = (\bar{t}, \bar{b}, D, \sigma)$ , where  $\bar{t}$  is a list of terms,  $\bar{b}$  is a list of tuples storing information needed for backtracking,  $D$  is a discrimination (sub)tree, and  $\sigma$  is a substitution.

Let  $D$  be a perfect discrimination tree.  $\mathcal{T}erm(D)$  denotes the set of terms stored in  $D$ . The function  $D|_\zeta$  returns a singleton set containing the subtree below  $D$ 's  $\zeta$  edge if it exists. If  $D$ 's root is accepting,  $val(D) = (s, d)$ , where  $s$  is the accepted term and  $d$  is some arbitrary data (e.g., an equation whose left- or right-hand side is  $t$ ).

Starting from an initial state  $([t], [], D, \emptyset)$ , where  $t$  is the query term and  $D$  is an entire discrimination tree, the following transitions are possible:

<b>AdvanceF</b>	$(f\ \bar{s}_m \cdot \bar{t}, \bar{b}, D, \sigma) \rightsquigarrow (\bar{s}_m \cdot \bar{t}, (f, m, D, \sigma) \cdot \bar{b}, D _f, \sigma)$ if $D _f$ is defined
<b>AdvanceX</b>	$(s\ \bar{s}_m \cdot \bar{t}, \bar{b}, D, \sigma) \rightsquigarrow (\bar{s}_m \cdot \bar{t}, (s, m, D, \sigma) \cdot \bar{b}, D _x, \sigma[x \mapsto s])$ if $D _x$ is defined, $x$ and $s$ have the same type, and $\sigma(x)$ is either undefined or equal to $s$
<b>Backtrack</b>	$(\bar{s}_m \cdot \bar{t}, (s, m, D_0, \sigma_0) \cdot \bar{b}, D, \sigma) \rightsquigarrow (s\ \bar{s}_m \cdot \bar{t}, \bar{b}, D_0, \sigma_0)$
<b>Success</b>	$([], \bar{b}, D, \sigma) \rightsquigarrow (val(D), \sigma)$ if $val(D)$ is defined

In the rules,  $\cdot$  denotes prepending an element or a list to a list, and  $D|_\zeta$  denotes the subtree labeled by  $\zeta$ , if it exists. Intuitively, **AdvanceF** and **AdvanceX** move deeper in the tree, generalizing cases A and B above to  $\lambda$ fHOL terms. **Backtrack** can be used to move back to a previous state. **Success** rule extracts the term  $t$  and data  $d$  stored in an accepting node.

The following derivation illustrates how to locate a generalization of  $g\ (g\ a\ b)$  in the tree  $D_2$ :

	$([g\ (g\ a\ b)], [], D, \{\})$
$\rightsquigarrow$ <b>AdvanceF</b>	$([g\ a\ b], [(g, 1, D, \emptyset)], D _g, \{\})$
$\rightsquigarrow$ <b>AdvanceX</b>	$([b], [(g\ a, 1, D _g, \emptyset), (g, 1, D, \emptyset)], D _{g.y}, \{y \mapsto g\ a\})$
$\rightsquigarrow$ <b>AdvanceX</b>	$([], [(b, 0, D _{g.y}, \{y \mapsto g\ a\}), (g\ a, 1, D _g, \emptyset), (g, 1, D, \emptyset)], D _{g.y.x}, \{y \mapsto g\ a, x \mapsto b\})$
$\rightsquigarrow$ <b>Success</b>	$((g\ (y\ x), d), \{y \mapsto g\ a, x \mapsto b\})$

Let  $\rightsquigarrow_{\text{Advance}} = \rightsquigarrow_{\text{AdvanceF}} \cup \rightsquigarrow_{\text{AdvanceX}}$ . It is easy to show that **Backtrack** undoes an **Advance** transition and can be avoided when starting from an initial state.

**Lemma 8.** *If  $\mathcal{Q} \rightsquigarrow_{\text{Advance}} \mathcal{Q}'$ , then  $\mathcal{Q}' \rightsquigarrow_{\text{Backtrack}} \mathcal{Q}$ .*

*Proof.* There are two cases to check:

$$\begin{aligned} (\mathbf{f} \overline{s_m} \cdot \overline{t}, \overline{b}, D, \sigma) &\rightsquigarrow_{\text{AdvanceF}} (\overline{s_m} \cdot \overline{t}, (\mathbf{f}, m, D, \sigma) \cdot \overline{b}, D|_{\mathbf{f}}, \sigma) \\ &\rightsquigarrow_{\text{Backtrack}} (\mathbf{f} \overline{s_m} \cdot \overline{t}, \overline{b}, D, \sigma) \\ (s \overline{s_m} \cdot \overline{t}, \overline{b}, D, \sigma) &\rightsquigarrow_{\text{AdvanceX}} (\overline{s_m} \cdot \overline{t}, (s, m, D, \sigma) \cdot \overline{b}, D|_x, \sigma[x \mapsto s]) \\ &\rightsquigarrow_{\text{Backtrack}} (s \overline{s_m} \cdot \overline{t}, \overline{b}, D, \sigma) \quad \square \end{aligned}$$

**Lemma 9.** *If  $\mathcal{Q} \rightsquigarrow_{\text{Advance}} \mathcal{Q}' \rightsquigarrow_{\text{Backtrack}} \mathcal{Q}''$ , then  $\mathcal{Q}'' = \mathcal{Q}$ .*

*Proof.* By Lemma 8,  $\mathcal{Q}' \rightsquigarrow_{\text{Backtrack}} \mathcal{Q}$ . Moreover, by inspecting its definition, we see that **Backtrack** is functional. Therefore,  $\mathcal{Q}'' = \mathcal{Q}$ .  $\square$

**Lemma 10.** *Let  $\mathcal{Q}_0 = ([t], [], D, \emptyset)$ . If  $\mathcal{Q}_0 \rightsquigarrow^* \mathcal{Q}'$ , then  $\mathcal{Q}_0 \rightsquigarrow_{\text{Advance}}^* \mathcal{Q}'$ .*

*Proof.* Let  $\mathcal{Q}_0 \rightsquigarrow \dots \rightsquigarrow \mathcal{Q}_n = \mathcal{Q}'$ . Let  $i$  be the index of the first transition  $\mathcal{Q}_i \rightsquigarrow_{\text{Backtrack}} \mathcal{Q}_{i+1}$ . Since  $\mathcal{Q}_0$ 's backtracking stack is empty, we must have  $i \neq 0$ . Hence we have  $\mathcal{Q}_{i-1} \rightsquigarrow_{\text{Advance}} \mathcal{Q}_i \rightsquigarrow_{\text{Backtrack}} \mathcal{Q}_{i+1}$ . By Lemma 9,  $\mathcal{Q}_{i-1} = \mathcal{Q}_{i+1}$ . Thus, we can shorten the derivation to  $\mathcal{Q}_0 \rightsquigarrow \dots \rightsquigarrow \mathcal{Q}_{i-1} = \mathcal{Q}_{i+1} \rightsquigarrow \dots \rightsquigarrow \mathcal{Q}_n$ , thereby eliminating one **Backtrack** transition. By repeating this process, we can eliminate all applications of **Backtrack**.  $\square$

**Lemma 11.** *There exist no infinite chains  $\mathcal{Q}_0 \rightsquigarrow_{\text{Advance}} \mathcal{Q}_1 \rightsquigarrow_{\text{Advance}} \dots$ .*

*Proof.* With each **Advance** transition, the height of the discrimination tree decreases by at least one. In an infinite chain, we would eventually reach a tree of height 0, consisting of a single node, from which no **Advance** transition is possible, contradicting the chain's existence.  $\square$

Perfect discrimination trees match a single term against a set of terms. To show that there are correct, we will connect them to the transition system  $\implies$  for matching (Sect. 4). This connection will help us show that whenever discrimination tree stores a generalization of a query term, this generalization can be found. To express the refinement argument, we introduce an intermediate transition system,  $\longleftrightarrow$ , that focuses on a single pair of terms (like  $\implies$ ) but that solve the constraints in a depth-first, left-to-right fashion and build the substitution incrementally (like  $\rightsquigarrow$ ). Its initial states are of the form  $([s \lesssim^? t], \emptyset)$ . Its transitions are as follows:

$$\begin{aligned} \text{Decompose} \quad &(\mathbf{f} \overline{s_m} \lesssim^? \mathbf{f} \overline{t_m} \cdot \overline{c}, \sigma) \longleftrightarrow ((s_1 \lesssim^? t_1, \dots, s_m \lesssim^? t_m) \cdot \overline{c}, \sigma) \\ \text{DecomposeX} \quad &(x \overline{s_m} \lesssim^? u \overline{t_m} \cdot \overline{c}, \sigma) \longleftrightarrow ((s_1 \lesssim^? t_1, \dots, s_m \lesssim^? t_m) \cdot \overline{c}, \sigma[x \mapsto u]) \\ &\text{if } x \text{ and } u \text{ have the same types and either } \sigma(x) \text{ is undefined or } \sigma(x) = u \\ \text{Success} \quad &([], \sigma) \longleftrightarrow \sigma \end{aligned}$$



Clash	$(f \overline{s}_m \lesssim? g \overline{t}_n \cdot \overline{c}, \sigma) \longleftrightarrow \perp$
ClashTypeX	$(x \overline{s}_m \lesssim? u \overline{t}_m \cdot \overline{c}, \sigma) \longleftrightarrow \perp$ if $x$ and $u$ have different types
ClashLenXF	$(x \overline{s}_m \lesssim? f \overline{t}_n \cdot \overline{c}, \sigma) \longleftrightarrow \perp$ if $m > n$
ClashLenXY	$(x \overline{s}_m \lesssim? y \overline{t}_n \cdot \overline{c}, \sigma) \longleftrightarrow \perp$ if $x \neq y$ and $m > n$
ClashFX	$(f \overline{s} \lesssim? x \overline{t} \cdot \overline{c}, \sigma) \longleftrightarrow \perp$
Double	$(x \overline{s}_m \lesssim? u \overline{t}_m \cdot \overline{c}, \sigma) \longleftrightarrow \perp$ if $x$ and $u$ have the same type, $\sigma(x)$ is defined, and $\sigma(x) \neq u$

We need an auxiliary function to convert  $\longleftrightarrow$  states to  $\Longrightarrow$  states. Let

$$\begin{aligned} \alpha(\overline{c}) &= \{s \lesssim? t \mid s \lesssim? t \in \overline{c}\} & \alpha(\sigma) &= \{x \lesssim? t \mid \sigma \text{ is defined on } x \text{ and } \sigma(x) = t\} \\ \alpha(\overline{c}, \sigma) &= \alpha(\overline{c}) \cup \alpha(\sigma) & \alpha(\perp) &= \perp \end{aligned}$$

Moreover, let  $\mathcal{S}$  range over states of the form  $(\overline{c}, \sigma)$  and  $\mathcal{R}$  additionally range over special states of the form  $\sigma$  or  $\perp$ .

**Lemma 12.** *If  $\mathcal{S} \longleftrightarrow \mathcal{R}$ , then  $\alpha(\mathcal{S}) \Longrightarrow^* \alpha(\mathcal{R})$ .*

*Proof.* By case distinction on  $\mathcal{R}$ . Let  $\mathcal{S} = (\overline{c}, \sigma)$ .

CASE  $\mathcal{R} = (\overline{c}', \sigma')$ : The only possible rules are  $\longleftrightarrow_{\text{Decompose}}$  and  $\longleftrightarrow_{\text{DecomposeX}}$ . If  $\longleftrightarrow_{\text{Decompose}}$  is applied, then  $\Longrightarrow_{\text{Decompose}}$  is applicable and results in  $\alpha(\mathcal{R})$ . If  $\longleftrightarrow_{\text{DecomposeX}}$  is applied, either we have  $m > 0$ , and  $\Longrightarrow_{\text{DecomposeX}}$  is applicable, or  $m = 0$ , and  $\alpha(\overline{c}', \sigma') = \alpha(\mathcal{S})$ , which implies that the two states are connected by an idle transition of  $\Longrightarrow^*$ .

CASE  $\mathcal{R} = \perp$ : All the  $\longleftrightarrow$  rules resulting in  $\perp$  except for **Double**, have the same side conditions as the corresponding  $\Longrightarrow$  rules.  $\longleftrightarrow_{\text{Double}}$  rule corresponds to  $\Longrightarrow_{\text{Double}}$  rule if  $m = 0$ . However, if  $m \neq 0$ , we need an intermediate  $\Longrightarrow_{\text{DecomposeX}}$  step before rule  $\Longrightarrow_{\text{Double}}$  can be applied to derive  $\perp$ . Namely, since  $\longleftrightarrow_{\text{Double}}$  is applicable, we have that  $\sigma(x) = u' \neq u$ . Hence,  $x \lesssim? u'$  must be present in  $\alpha(\overline{c}, \sigma)$ . Rule  $\Longrightarrow_{\text{DecomposeX}}$  will augment this set with  $x \lesssim? u$ , enabling  $\Longrightarrow_{\text{Double}}$ .

CASE  $\mathcal{R} = \sigma$ : The only possible rule is  $\longleftrightarrow_{\text{Success}}$ , with  $\overline{c} = []$ . Since  $\alpha(\mathcal{S}) = \alpha(\sigma)$ , this transition corresponds to an idle transition of  $\Longrightarrow^*$ .  $\square$

**Lemma 13.** *If  $\mathcal{S} \longleftrightarrow^! \mathcal{R}$ , then  $\mathcal{R}$  is either some substitution  $\sigma'$  or  $\perp$ . If  $\mathcal{S} \longleftrightarrow^! \sigma'$ , then  $\sigma'$  is the MGG of  $\alpha(\mathcal{S})$ . If  $\mathcal{S} \longleftrightarrow^! \perp$ , then  $\alpha(\mathcal{S})$  has no solutions.*

*Proof.* First, we show that states  $\mathcal{S}' = (\overline{c}', \sigma')$  cannot be normal forms, by exhibiting transitions from such states. If  $\overline{c}' = []$ , the  $\longleftrightarrow_{\text{Success}}$  rule would apply. Otherwise, let  $\overline{c}' = c_1 \cdot \overline{c}''$ , and consider the matching problem  $\{c_1\} \cup \alpha(\sigma')$ . If this problem is in solved form,  $c_1$  is a constraint corresponding to a solved variable, and we can apply  $\longleftrightarrow_{\text{DecomposeX}}$  to move the constraint into the substitution. Otherwise, some  $\Longrightarrow$  rule can be applied. This rule necessarily focuses on  $c_1$ , since the constraints from  $\alpha(\sigma')$  correspond to solved variables. The homologous  $\longleftrightarrow$  rule can be applied to  $\mathcal{S}'$ .

Second, by Lemma 12, if  $\mathcal{S} \xrightarrow{!} \sigma'$ , then  $\alpha(\mathcal{S}) \Longrightarrow^* \alpha(\sigma')$ . By construction,  $\alpha(\sigma')$  is in solved form. Thus,  $\alpha(\mathcal{S}) \Longrightarrow^! \alpha(\sigma')$ . By Theorem 6, the substitution corresponding to  $\alpha(\sigma')$ —that is,  $\sigma'$ —is the MGG of  $\alpha(\mathcal{S})$ .

Third, by Lemma 12, if  $\mathcal{S} \xrightarrow{!} \perp$ , then  $\alpha(\mathcal{S}) \Longrightarrow^! \perp$ . By Theorem 6,  $\alpha(\mathcal{S})$  has no solutions.  $\square$

**Lemma 14.** *The relation  $\xrightarrow{!}$  is well founded.*

By Lemma 12, every  $\xrightarrow{!}$  transition corresponds zero or more  $\Longrightarrow$  transitions. Since  $\Longrightarrow$  is well founded (by Theorem 7), the only transitions that can violate well-foundedness of  $\xrightarrow{!}$  are the ones that perform idle  $\Longrightarrow$  transitions:  $\xrightarrow{\text{DecomposeX}}$  for  $m = 0$  and  $\xrightarrow{\text{Success}}$ . The latter is terminal, so it cannot contribute to an infinite  $\xrightarrow{!}$  chains. And since  $\xrightarrow{\text{DecomposeX}}$ , with  $m = 0$ , decreases the length of  $\alpha(\bar{c})$ , it can be applied at most finitely many times. Thus  $\xrightarrow{!}$  is well founded.

**Lemma 15.** *If term  $s$  generalizes term  $t$ , then  $([s \lesssim^? t], \emptyset) \xrightarrow{!} \sigma$ , where  $\sigma$  is the MGG of  $s \lesssim^? t$ .*

*Proof.* By Lemma 14, there exists a normal form  $\mathcal{R}$  starting from  $\mathcal{S} = ([s \lesssim^? t], \emptyset)$ . Since  $s \lesssim^? t$  is solvable, by Lemma 13,  $\mathcal{R}$  must be the MGU for  $s$  and  $t$ .  $\square$

**Lemma 16.** *If there exists a term  $s \in \text{Term}(D)$  that generalizes the query term  $t$ , then there exists a derivation  $([t], [], D, \emptyset) \rightsquigarrow^! ((s, d), \sigma)$ .*

*Proof.* By Lemma 15, we know that  $(s \lesssim^? t, \emptyset) \xrightarrow{!} \sigma$  for each  $s \in \text{Term}(D)$  generalizing  $t$ . This means that there exists a derivation of the form  $([s \lesssim^? t], \emptyset) = \mathcal{S}_0 \xrightarrow{!} \mathcal{S}_1 \xrightarrow{!} \dots \xrightarrow{!} \mathcal{S}_n \xrightarrow{!} \sigma$ . The  $n$  first transitions must be `Decompose` or `DecomposeX`, and the last transition must be `Success`.

We show that there exists a derivation of the form  $([t], [], D, \emptyset) = \mathcal{Q}_0 \rightsquigarrow \mathcal{Q}_1 \rightsquigarrow \dots \rightsquigarrow \mathcal{Q}_n \rightsquigarrow ((s, d), \sigma)$ , where  $\mathcal{Q}_i = (\bar{t}_i, \bar{b}_i, D_i, \sigma_i)$  for each  $i$ . We define  $\bar{t}_i$ ,  $D_i$ , and  $\bar{b}_i$  as follows, for  $i > 0$ :

- The term list  $\bar{t}_i$  consists of the right-hand sides of the constraints  $\bar{c}_i$ , in the same order.
- If a leaf node storing  $s$  was reached in  $n$  transitions, the serialization of  $s$  must be of the form  $\zeta_1 \cdot \dots \cdot \zeta_n$ . Take  $D_i = D_{i-1}|_{\zeta_i}$ .
- The backtracking information  $\bar{b}_i$  is set to  $(\zeta_i, m, D_{i-1}, \sigma_{i-1}) \cdot \overline{\bar{b}_{i-1}}$ , where  $m = |\bar{t}_i| - |\bar{t}_{i-1}| + 1$ .

It is easy to check that the sequence of quadruples  $\mathcal{Q}_i$  forms a derivation:

- If  $(\bar{c}_i, \sigma_i) \xrightarrow{\text{Decompose}} (\bar{c}_{i+1}, \sigma_{i+1})$ , then  $\mathcal{Q}_i \rightsquigarrow \text{AdvanceF} \mathcal{Q}_{i+1}$ .
- If  $(\bar{c}_i, \sigma_i) \xrightarrow{\text{DecomposeX}} (\bar{c}_{i+1}, \sigma_{i+1})$ , then  $\mathcal{Q}_i \rightsquigarrow \text{AdvanceX} \mathcal{Q}_{i+1}$ .
- If  $(\bar{c}_n, \sigma_n) \xrightarrow{\text{Success}} \sigma$ , then  $\mathcal{Q}_n \rightsquigarrow \text{Success} ((s, d), \sigma)$ .  $\square$

**Lemma 17.** *If  $([t], [], D, \emptyset) \rightsquigarrow^+ ((s, d), \sigma)$ , then  $s \in \text{Term}(D)$  and  $\sigma$  is the MGG of  $s \lesssim^? t$ .*

*Proof.* Let  $([t], [], D, \emptyset) = \mathcal{Q}_0 \rightsquigarrow \mathcal{Q}_1 \rightsquigarrow \dots \rightsquigarrow \mathcal{Q}_n \rightsquigarrow ((s, d), \sigma)$  be a derivation, where  $\mathcal{Q}_i = (\bar{t}_i, \bar{b}_i, D_i, \sigma_i)$  for each  $i$ . Without loss of generality, by Lemma 10, we can assume that the derivation contains no **Backtrack** transitions.

The first conjunct,  $s \in \text{Term}(D)$ , holds by definition for any term found from an initial state. To prove the second conjunct, we first introduce a function *preord* that defines the preorder decomposition of a list of terms:

$$\begin{aligned} \text{preord}([]) &= [] \\ \text{preord}(\zeta \bar{s}_n \cdot \bar{x}s) &= (\zeta, \bar{s}_n \cdot \bar{x}s) \cdot \text{preord}(\bar{s}_n \cdot \bar{x}s) \end{aligned}$$

Given a term  $s$ ,  $\text{preord}([s])$  gives a sequence  $(\zeta_1, \overline{\text{args}}_1), \dots, (\zeta_n, \overline{\text{args}}_n)$ . Since  $s \in \text{Term}(D)$ , the sequence  $D_0, \dots, D_n$  follows the serialization of  $s$ :  $D_i = D_{i-1}|_{\zeta_i}$  for each  $i > 0$ .

Next, we show that there exists a derivation of the form  $([s \lesssim^? t], \emptyset) = \mathcal{S}_0 \xrightarrow{\dots} \mathcal{S}_n \xrightarrow{\dots} \sigma$ , where  $\mathcal{S}_i = (\bar{c}_i, \sigma_i)$ . We define  $\bar{c}_i$ , for  $i > 0$ , as the list of constraints whose left-hand sides are  $\overline{\text{args}}_i$  and right-hand sides are  $\bar{t}_i$ . Both lists have the same length.

We can then check that the sequence of states  $\mathcal{S}_i$  forms a derivation:

- If  $\mathcal{Q}_i \rightsquigarrow_{\text{AdvanceF}} \mathcal{Q}_{i+1}$ , then  $\mathcal{S}_i \xrightarrow{\text{Decompose}} \mathcal{S}_{i+1}$ .
- If  $\mathcal{Q}_i \rightsquigarrow_{\text{AdvanceX}} \mathcal{Q}_{i+1}$ , then  $\mathcal{S}_i \xrightarrow{\text{DecomposeX}} \mathcal{S}_{i+1}$ .
- If  $\mathcal{Q}_n \rightsquigarrow_{\text{Success}} \sigma$ , then  $\mathcal{S}_n \xrightarrow{\text{Success}} \sigma$ . □

**Theorem 18 (Total Correctness).** *Let  $D$  be a perfect discrimination tree and  $t$  be a term. The sets  $\{s \in \text{Term}(D) \mid \exists \sigma. \sigma(s) = t\}$  and  $\{s \mid \exists d, \sigma. ([t], [], D, \emptyset) \rightsquigarrow^! ((s, d), \sigma)\}$  are equal.*

*Proof.* This follows from Lemmas 16 and 17. □

The above theorem states that all generalizations  $s$  of a term  $t$  stored in the perfect discrimination tree can be found, but it does not exclude nondeterminism. Often, both **AdvanceF** and **AdvanceX** are applicable. To find all generalizations, we need to follow both transitions. But for some applications, it is enough to find a single generalization.

To cater for both styles, E provides iterators that store the current state of a traversal through a discrimination tree. After the iterator is initialized with the root node  $D$  and the query term  $t$ , each call to **FINDNEXTVAL** will move the iterator to the next node that generalizes the query term and stores a value, indicating an accepting node. After all such nodes have been traversed, the iterator is set to point to *Null*.

The pseudocode for Ehoh's updated iterator-based functions and procedures is given below. The following definitions constitute the high-level interface for iterating through values incrementally or for obtaining all values.

```
function INITITER(PDTNode  $D$ , Term  $t$ ) is
   $i \leftarrow$  ITERATOR()
   $(i.\text{node}, i.\text{term\_stack}, i.\text{term\_proc}, i.\text{child\_iter}) \leftarrow (D, [t], [], \text{Start})$ 
  return  $i$ 
```

```

procedure FINDNEXTVAL(Iterator i) is
  do
    FINDNEXTNODE(i)
    while i.node ≠ Null ∧ (¬i.term_stack.isEmpty() ∨ ¬i.node.has_val())
function ALLVALS(PDTNode D, Term t) is
  i ← INITITER(D, t)
  hit ← FINDNEXTVAL(i)
  res ← ∅
  while i.node ≠ Null do
    res ← res ∪ {i.node.val()}
    FINDNEXTVAL(i)
  return res

```

The core logic is implemented in FINDNEXTNODE, presented below. It visits edges labeled with variables before visiting edges labeled with symbols. We assume that we can iterate through the children of a node using a function NEXTVARCHILD that, given a tree node and iterator through children, advances the iterator to the child corresponding to the next variable. (There is no need to iterate through symbols, because at most one such child should be visited.) Furthermore, we assume that the iterator can also be in the distinguished states *Start* and *End*. *Start* indicates that no child has been visited yet, whereas *End* indicates that we have visited all children.

We also rely on a function CHILD that returns a child corresponding to a symbol or a variable, if one exists, or *Null* otherwise. The information we need for backtracking includes the last visited child of the node and whether a variable was bound.

```

procedure BACKTRACKTOVAR(Iterator i) is
  forever do
    if i.term_proc.isEmpty() then
      i.node ← Null
      return
    else
      (t, D, child_iter, n, x) ← i.term_proc.pop()
      for i ← 1 to n do
        i.term_stack.pop()
        i.term_stack.push(t)
        i.node ← D
        i.child_iter ← child_iter
        if x ≠ Null then
          x.binding ← Null
        if child_iter ≠ End then
          return
procedure FINDNEXTNODE(Iterator i) is
  if i.term_stack.isEmpty() then

```

```

    BACKTRACKTOVAR(i)
advanced ← False
while i.node ≠ Null ∧ ¬ advanced do
  while i.child_iter ≠ End ∧ ¬ advanced do
    i.child_iter ← NEXTVARCHILD(i.node, i.child_iter)
    if i.child_iter ≠ End then
      x ← i.child_iter.var()
      t ← i.term_stack.top()
      prefix ← GOBBLEPREFIX(x, t)
      if prefix ≠ Null ∧ (x.binding = Null ∨ x.binding = prefix) then
        i.term_stack.pop()
        pushed ← t.num_args − prefix.num_args
        for j ← t.num_args() downto prefix.num_args + 1 do
          i.term_stack.push(t.args[j])
        if x.binding = Null then
          x.binding ← prefix
          i.term_proc.push((t, i.node, i.child_iter, pushed, x))
        else
          i.term_proc.push((t, i.node, i.child_iter, pushed, Null))
        i.node ← CHILD(i.node, x)
        advanced ← True
      t ← i.term_stack.top()
    if i.child_iter = End
      ∧ ¬ t.head().isVar() ∧ CHILD(D, t.head()) ≠ Null then
        i.term_stack.pop()
        for j ← t.num_args downto 1 do
          i.term_stack.push(t.args[j])
        i.term_proc.push((t, i.node, i.child_iter, t.num_args, Null))
        i.node ← CHILD(i.node, t.head())
        advanced ← True
    if ¬ advanced then
      BACKTRACKTOVAR(i)
    else
      i.child_iter ← Start

```

## 5.2 Fingerprint Indices

Fingerprint indices [40] trade perfect indexing for a compact memory representation and more flexible retrieval conditions. The basic idea is to compare terms by looking only at a few predefined sample positions. If we know that term  $s$  has symbol  $f$  at the head of the subterm at 2.1 and term  $t$  has  $g$  at the same position, we can immediately conclude that  $s$  and  $t$  are not unifiable.

Let A (“at a variable”), B (“below a variable”), and N (“nonexistent”) be distinguished symbols not present in the signature. Given a term  $t$  and a position  $p$ ,

the *fingerprint function*  $Gfpf$  is defined as

$$Gfpf(t, p) = \begin{cases} f & \text{if } t|_p \text{ has a symbol head } f \\ A & \text{if } t|_p \text{ is a variable} \\ B & \text{if } t|_q \text{ is a variable for some proper prefix } q \text{ of } p \\ N & \text{otherwise} \end{cases}$$

Based on a fixed tuple of sample positions  $\overline{p_n}$ , the *fingerprint* of a term  $t$  is defined as  $\mathcal{F}p(t) = (Gfpf(t, p_1), \dots, Gfpf(t, p_n))$ . To compare two terms  $s$  and  $t$ , it suffices to check that their fingerprints are componentwise compatible using the following unification and matching matrices:

	f <sub>1</sub>	f <sub>2</sub>	A	B	N
f <sub>1</sub>		<b>X</b>			<b>X</b>
f <sub>2</sub>	<b>X</b>				<b>X</b>
A					<b>X</b>
B					
N	<b>X</b>	<b>X</b>	<b>X</b>		

	f <sub>1</sub>	f <sub>2</sub>	A	B	N
f <sub>1</sub>		<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>
f <sub>2</sub>	<b>X</b>		<b>X</b>	<b>X</b>	<b>X</b>
A				<b>X</b>	<b>X</b>
B					
N	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	

The rows and columns correspond to  $s$  and  $t$ , respectively. The metavariables  $f_1$  and  $f_2$  represent arbitrary distinct symbols. Incompatibility is indicated by **X**.

As an example, let  $(\epsilon, 1, 2, 1.1, 1.2, 2.1, 2.2)$  be the sample positions, and let  $s = f(a, x)$  and  $t = f(g(x), g(a))$  be the terms to unify. Their fingerprints are

$$\begin{aligned} \mathcal{F}p(s) &= (f, a, A, N, N, B, B) \\ \mathcal{F}p(t) &= (f, g, g, A, N, a, N) \end{aligned}$$

Using the left matrix, we compute the compatibility vector  $(-, \mathbf{X}, -, \mathbf{X}, -, -, -)$ . The mismatches at positions 1 and 1.1 indicate that  $s$  and  $t$  are not unifiable.

A fingerprint index is a trie that stores a term set  $T$  keyed by fingerprint. The term  $f(g(x), g(a))$  above would be stored in the node addressed by  $f.g.g.A.N.a.N$ , possibly together with other terms that share the same fingerprint. This organization makes it possible to unify or match a query term  $s$  against all the terms  $T$  in one traversal. Once a node storing the terms  $U \subseteq T$  has been reached, due to overapproximation we must apply unification or matching on  $s$  and each  $u \in U$ .

When adapting this data structure to  $\lambda$ FHOL, we must first choose a suitable notion of position in a term. Conventionally, higher-order positions are strings over  $\{1, 2\}$  indicating, for each binary application  $t_1 t_2$ , which term  $t_i$  to follow. Given that this is not graceful, it seems preferable to generalize the first-order notion to flattened  $\lambda$ FHOL terms—e.g.,  $x a b|_1 = a$  and  $x a b|_2 = b$ . However, this approach fails on applied variables. For example, although  $x b$  and  $f a b$  are unifiable (using  $\{x \mapsto f a\}$ ), sampling position 1 would yield a clash between  $b$  and  $a$ . To ensure that positions remain stable under substitution, we propose to number arguments in reverse:

$$t|^\epsilon = t \qquad \zeta t_n \dots t_1|^{i.p} = t_i|^{i.p} \quad \text{if } 1 \leq i \leq n$$

We use a nonstandard notation,  $t|^{i.p}$ , for this nonstandard notion. The operation is undefined for out-of-bound indices.

**Lemma 19.** *Let  $s$  and  $t$  be unifiable terms, and let  $p$  be a position such that the subterms  $s|_p$  and  $t|_p$  are defined. Then  $s|_p$  and  $t|_p$  are unifiable.*

*Proof.* By structural induction on  $p$ .

CASE  $p = \epsilon$ : Trivial.

CASE  $p = q.i$ : Let  $s|_q = \zeta s_m \dots s_1$  and  $t|_q = \eta t_n \dots t_1$ . Since  $p$  is defined in both  $s$  and  $t$ , we have  $s|_p = s_i$  and  $t|_p = t_i$ . By the induction hypothesis,  $s|_q$  and  $t|_q$  are unifiable, meaning that there exists a substitution  $\sigma$  such that  $\sigma(\zeta s_m \dots s_1) = \sigma(\eta t_n \dots t_1)$ . Hence,  $\sigma(s_1) = \sigma(t_1), \dots, \sigma(s_i) = \sigma(t_i)$ . Thus,  $\sigma(s|_p) = \sigma(t|_p)$ .  $\square$

Let  $t|_p$  denote the subterm  $t|_q$  such that  $q$  is the longest prefix of  $p$  for which  $t|_q$  is defined. The  $\lambda$ fHOL version of the fingerprint function is defined as follows:

$$\mathcal{Gfpf}'(t, p) = \begin{cases} \mathbf{f} & \text{if } t|_p \text{ has a symbol head } \mathbf{f} \\ \mathbf{A} & \text{if } t|_p \text{ has a variable head} \\ \mathbf{B} & \text{if } t|_p \text{ is undefined but } t|_p \text{ has a variable head} \\ \mathbf{N} & \text{otherwise} \end{cases}$$

Except for the reversed numbering scheme,  $\mathcal{Gfpf}'$  coincides with  $\mathcal{Gfpf}$  on first-order terms. The fingerprint  $\mathcal{Fp}'(t)$  of a term  $t$  is defined analogously as before, and the same compatibility matrices can be used.

The most interesting new case is that of an applied variable. Given the sample positions  $(\epsilon, 2, 1)$ , the fingerprint of  $x$  is  $(\mathbf{A}, \mathbf{B}, \mathbf{B})$  as before, whereas the fingerprint of  $x \mathbf{c}$  is  $(\mathbf{A}, \mathbf{B}, \mathbf{c})$ . As another example, let  $(\epsilon, 2, 1, 2.2, 2.1, 1.2, 1.1)$  be the sample positions, and let  $s = x \mathbf{f} \mathbf{b} \mathbf{c}$  and  $t = \mathbf{g} \mathbf{a} \mathbf{y} \mathbf{d}$ . Their fingerprints are

$$\begin{aligned} \mathcal{Fp}'(s) &= (\mathbf{A}, \mathbf{B}, \mathbf{f}, \mathbf{B}, \mathbf{B}, \mathbf{b}, \mathbf{c}) \\ \mathcal{Fp}'(t) &= (\mathbf{g}, \mathbf{a}, \mathbf{A}, \mathbf{N}, \mathbf{N}, \mathbf{B}, \mathbf{d}) \end{aligned}$$

The terms are not unifiable due to the incompatibility at position 1.1 ( $\mathbf{c}$  versus  $\mathbf{d}$ ).

We can easily support prefix optimization for both terms  $s$  and  $t$  being compared: We ensure that  $s$  and  $t$  are fully applied, by adding enough fresh variables as arguments, before computing their fingerprints.

Because fingerprint indexing is an imperfect method, its correctness theorem must be stated weakly. Terms that have incompatible fingerprint cannot be unified or matched, but the converse does not hold.

**Lemma 20.** *If  $s$  and  $t$  are unifiable, then  $\mathcal{Gfpf}'(s, p)$  and  $\mathcal{Gfpf}'(t, p)$  are compatible according to the unification matrix.*

*Proof.* By contraposition, it suffices to consider the eight blank cells in the unification matrix. Since unifiability is a symmetric relation, we can further restrict our focus to four cases:

$$\begin{aligned} \mathcal{Gfpf}'(s, p) &= \mathbf{f}_1 \ \mathbf{f}_1 \ \mathbf{f}_2 \ \mathbf{A} \\ \mathcal{Gfpf}'(t, p) &= \mathbf{f}_2 \ \mathbf{N} \ \mathbf{N} \ \mathbf{N} \end{aligned}$$

The last three cases admit the same argument.

CASE  $f_1, f_2$ : By definition of  $\mathcal{G}f'p'$ ,  $s|p$  and  $t|p$  must be of the forms  $f_1 \bar{s}$  and  $f_2 \bar{t}$ , respectively. Clearly,  $s|p$  and  $t|p$  are not unifiable. By Lemma 19,  $s$  and  $t$  are not unifiable.

CASE  $f_1, N$ ;  $f_2, N$ ; or  $A, N$ : From  $\mathcal{G}f'p'(s, p) = N$ , we deduce that  $p \neq \epsilon$ . Let  $p = q.i.r$ , where  $q$  is the longest prefix such that  $\mathcal{G}f'p'(t, q) \neq N$ . Since  $\mathcal{G}f'p'(t, q.i) = N$ , the head of  $t|q$  must be some symbol  $g$ . (For a variable head, we would have  $\mathcal{G}f'p'(t, q.i) = B$ .) Hence,  $t|q$  has the form  $g t_n \dots t_1$ , for  $n < i$ . Since  $q.i$  is a legal position in  $s$ ,  $s|q$  has the form  $\zeta s_m \dots s_1$ , with  $i \leq m$ . A necessary condition for  $\sigma(s|q) = \sigma(t|q)$  is that  $\sigma(\zeta s_m \dots s_{n+1}) = \sigma(g)$ , but this is impossible because the left-hand side is an application (since  $n < m$ ), whereas the right-hand side is the symbol  $g$ . By Lemma 19,  $s$  and  $t$  are not unifiable.  $\square$

**Lemma 21.** *If  $s$  generalizes  $t$ , then  $\mathcal{G}f'p'(s, p)$  and  $\mathcal{G}f'p'(t, p)$  are compatible according to the matching matrix.*

*Proof.* The proof is by case distinction, similarly to Lemma 20.  $\square$

**Theorem 22 (Overapproximation).** *If  $s$  and  $t$  are unifiable, then  $\mathcal{F}p'(s)$  and  $\mathcal{F}p'(t)$  are compatible according to the unification matrix. If  $s$  generalizes  $t$ , then  $\mathcal{F}p'(s)$  and  $\mathcal{F}p'(t)$  are compatible according to the matching matrix.*

*Proof.* This follows from Lemmas 20 and 21.

### 5.3 Feature Vector Indices

Subsumption is a crucial operation to prune the search space. A clause  $C$  subsumes another clause  $D$  if there exists a substitution  $\sigma$  such that  $\sigma(C) \subseteq D$ . Feature-vector indices [41] are an imperfect indexing data structure that can be used to retrieve clauses that subsume a query clause (forward subsumption) or that are subsumed by the query clause (backward subsumption).

Feature-vector indices are similar to fingerprint indices, but they cannot work positionally, because literals are unordered in a clause. Each clause is represented by a vector of numerical features. The features must be compatible with the subsumption relation in the following sense: For any feature  $f$ , whenever  $C$  subsumes  $D$ , we must have  $f(C) \leq f(D)$ . For example, the number of occurrences of a given symbol  $c$  is a legal feature.

Feature vectors are organized in a trie. When performing a lookup, we must consider all paths along which the query term's vector entries are pointwise greater (for forward subsumption) or less (for backward subsumption) than or equal to the corresponding trie nodes.

Unlike for discrimination trees and fingerprint indices, no changes were necessary to adapt feature vectors indices to  $\lambda$ fHOL. All the predefined features make sense in  $\lambda$ fHOL and are compatible with subsumption.



## 6 Inference Rules

Saturating provers try to show the unsatisfiability of a set of clauses by systematically adding logical consequences (up to simplification and redundancy), eventually deriving the empty clause as an explicit witness of unsatisfiability. They employ two kinds of inference rules: *generating rules* produce new clauses and are necessary for completeness, whereas *simplification rules* delete existing clauses or replace them by simpler clauses. This simplification is crucial for success, and most modern provers spend a large part of their time on simplification.

E implements a variant of the *given-clause* algorithm. The proof state is represented by two disjoint subsets of clauses, the *processed* clauses  $P$  and the *unprocessed* clauses  $U$ . Initially, all clauses are unprocessed. At each iteration of the main loop, the prover heuristically selects a *given clause* from  $U$ , adds it to  $P$ , and performs all generating inferences between this clause and all clauses in  $P$ . Resulting new clauses are added to  $U$ . This maintains the invariant that all direct consequences between clauses in  $P$  have been performed. Simplification is performed on the given clause (using clause in  $P$  as side premises), on clauses in  $P$  (using the given clause), and on newly generated clauses (again, using  $P$ ).

Ehoh implements essentially the same logical calculus as E, except that it is generalized to  $\lambda$ FHOL terms. The standard inference rules and completeness proof of superposition can be reused verbatim; the only changes concern the basic definitions of terms and substitutions [8, Sect. 1]. Completeness of superposition for  $\lambda$ FHOL terms has been formally proved by Peltier [35] using Isabelle.

### 6.1 The Generating Rules

The superposition calculus consists of the following four core generating rules, whose conclusions are added to the proof state:

$$\frac{s \not\approx s' \vee C}{\sigma(C)} \text{ER} \qquad \frac{s \approx t \vee s' \approx u \vee C}{\sigma(t \not\approx u \vee s \approx u \vee C)} \text{EF}$$

$$\frac{s \approx t \vee C \quad u[s'] \not\approx v \vee D}{\sigma(u[t] \not\approx v \vee C \vee D)} \text{SN} \qquad \frac{s \approx t \vee C \quad u[s'] \approx v \vee D}{\sigma(u[t] \approx v \vee C \vee D)} \text{SP}$$

In each rule,  $\sigma$  denotes the MGU of  $s$  and  $s'$ . Not shown are order- and selection-based side conditions that restrict the rules' applicability.

Equality resolution and factoring (ER and EF) are single-premise rules that work on entire terms that occur on either side of a literal occurring in the given clause. To generalize them, it suffices to disable prefix optimization for our unification algorithm.

The rules for superposition into negative and positive literals (SN and SP) are more complex. As two-premise rules, they require the prover to find a partner for the given clause. There are two cases to consider, depending on whether the given clause acts as the first or second premise in an inference. Moreover, since the

rules operate on subterms  $s'$  of a clause, it is important to be able to efficiently locate all relevant subterms, including  $\lambda$ fHOL-specific prefix subterms.

To cover the case where the given clause acts as the left premise, the prover relies on a fingerprint index to compute a set of clauses containing terms possibly unifiable with a side  $s$  of a positive literal of the given clause. Thanks to our generalization of fingerprints, in E<sub>hoh</sub> this candidate set is guaranteed to over-approximate the set of all possible inference partners. The unification algorithm is then applied to filter out unsuitable candidates. Thanks to prefix optimization, we can avoid gracelessly polluting the index with all prefix subterms.

For the case where the given clause is the right premise, the prover traverses its subterms  $s'$  looking for inference partners in another fingerprint index, which contains only entire left- and right-hand sides of equalities. Like E, E<sub>hoh</sub> traverses subterms in a first-order fashion. If prefix unification succeeds, E<sub>hoh</sub> determines the unified prefix and applies the appropriate inference instance.

## 6.2 The Simplifying Rules

Unlike generating rules, simplifying rules do not necessarily add conclusions to the proof state—they can also remove premises. E implements over a dozen simplifying rules, with unconditional rewriting and clause subsumption as the most significant examples. Here, we restrict our attention to a single rule, which best illustrates the challenges of supporting  $\lambda$ fHOL:

$$\frac{s \approx t \quad u[\sigma(s)] \approx u[\sigma(t)] \vee C}{s \approx t} \text{ES}$$

Given an equation  $s \approx t$ , equality subsumption (ES) removes a clause containing a literal whose two sides are equal except that an instance of  $s$  appears on one side where the corresponding instance of  $t$  appears on the other side.

E maintains a perfect discrimination tree that stores clauses of the form  $s \approx t$  indexed by  $s$  and  $t$ . When applying the ES rule, E considers each positive literal  $u \approx v$  of the given clause in turn. It starts by taking the left-hand side  $u$  as a query term. If an equation  $s \approx t$  (or  $t \approx s$ ) is found in the tree, with  $\sigma(s) = u$ , the prover checks whether  $\sigma'(t) = v$  for some (possibly nonstrict) extension  $\sigma'$  of  $\sigma$ . If so, ES is applicable, with a second premise of the form  $\sigma(s) \approx \sigma(t) \vee C$ .

To consider nonempty contexts, the prover traverses the subterms  $u'$  and  $v'$  of  $u$  and  $v$  in lockstep, as long as they appear under identical contexts. Thanks to prefix optimization, when E<sub>hoh</sub> is given a subterm  $u'$ , it can find an equation  $s \approx t$  in the tree such that  $\sigma(s)$  is equal to some prefix of  $u'$ , with  $n$  arguments  $\overline{u_n}$  remaining as unmatched. Checking for equality subsumption then amounts to checking that  $v' = \sigma'(t) \overline{u_n}$ , for some extension  $\sigma'$  of  $\sigma$ .

For example, let  $f(\mathbf{g} \ \mathbf{a} \ \mathbf{b}) \approx f(\mathbf{h} \ \mathbf{g} \ \mathbf{b})$  be the given clause, and suppose that  $x \ \mathbf{a} \approx \mathbf{h} \ x$  is indexed. Under context  $f[\ ]$ , E<sub>hoh</sub> considers the subterms  $\mathbf{g} \ \mathbf{a} \ \mathbf{b}$  and  $\mathbf{h} \ x \ \mathbf{b}$ . It finds the prefix  $\mathbf{g} \ \mathbf{a}$  of  $\mathbf{g} \ \mathbf{a} \ \mathbf{b}$  in the tree, with  $\sigma = \{x \mapsto \mathbf{g}\}$ . The prefix  $\mathbf{h} \ \mathbf{g}$  of  $\mathbf{h} \ \mathbf{g} \ \mathbf{b}$  matches the indexed equation's right-hand side  $\mathbf{h} \ x$  using the same substitution, and the remaining argument in both subterms,  $\mathbf{b}$ , is identical. E<sub>hoh</sub> concludes that the given clause is redundant.

## 7 Heuristics

E’s heuristics are largely independent of the prover’s logic and work unchanged for Ehoh. On first-order problems, Ehoh’s behavior is virtually the same as E’s. Yet, in preliminary experiments, we observed that some  $\lambda$ fHOL benchmarks were proved quickly by E in conjunction with the applicative encoding (Sect. 1) but timed out with Ehoh. There were enough problems of this kind to prompt us to take a closer look. Based on these observations, we extended the heuristics to exploit  $\lambda$ fHOL-specific features.

### 7.1 Term Order Generation

The inference rules and the redundancy criterion are parameterized by a term order—typically an instance of KBO or LPO (Sect. 3). E can generate a *symbol weight* function (for KBO) and a *symbol precedence* (for KBO and LPO) based on criteria such as the symbols’ frequencies, their arities, and whether they appear in the conjecture.

In preliminary experiments, we discovered that the presence of an explicit application operator  $@$  can be beneficial for some problems. A small example will help illustrate this behavior. Let  $a : \iota_1$ ,  $b : \iota_2$ ,  $c : \iota_3$ ,  $f : \iota_1 \rightarrow \iota_2 \rightarrow \iota_3$ ,  $x : \iota_2 \rightarrow \iota_3$ ,  $y : \iota_2$ , and  $z : \iota_3$ , and consider the clauses

$$f\ a\ y \not\approx c \qquad x\ b \approx z$$

where the first one is the negated conjecture. Their applicative encoding is

$$@_{\iota_2, \iota_3} (@_{\iota_1, \iota_2 \rightarrow \iota_3} (f, a), y) \not\approx c \qquad @_{\iota_2, \iota_3} (x, b) \approx z$$

where  $@_{\tau, v}$  is a type-indexed family of explicit application symbols representing the application of a function of type  $\tau \rightarrow v$ . With the applicative encoding, generation schemes can take the symbols  $@_{\tau, v}$  into account, effectively exploiting the type information carried by such symbols. Since  $@_{\iota_2, \iota_3}$  is a conjecture symbol, some weight generation scheme could give it a low weight, which would also impact the second clause. By contrast, the native  $\lambda$ fHOL clauses share no symbols; the connection between them is hidden in the types of variables and symbols, which are ignored by the heuristics.

To simulate the desirable behavior observed on applicatively encoded problems, we introduced four generation schemes that extend E’s existing symbol-frequency-based schemes by partitioning the symbols by type. To each symbol, the new schemes assign a frequency corresponding to the sum of all symbol frequencies for its class. Each new scheme is inspired by a similarly named type-agnostic scheme in E, without `type` in its name:

- `typefreqcount` assigns as each symbol’s weight the number of occurrences of symbols that have the same type.
- `typefreqrank` sorts the frequencies obtained using `typefreqcount` in increasing order and assigns each symbol a weight corresponding to its rank.

- `invtypefreqcount` is `typefreqcount`'s inverse. Whenever `typefreqcount` would assign a weight  $w$  to a symbol, it assigns  $M - w + 1$ , where  $M$  is the maximum symbol weight according to `typefreqcount`.
- `invtypefreqrank` is `typefreqrank`'s inverse. It sorts the frequencies in decreasing order.

In addition, we designed four schemes that combine E's type-agnostic and Ehoh's type-aware approaches using a linear equation:

- `combfreqcount` assigns as each symbol's weight the value  $2w_1 + w_2$ , where  $w_1$  is the weight according to `freqcount` (i.e., the symbol's frequency) and  $w_2$  is the weight according to `typefreqcount`.
- `combfreqrank` sorts the frequencies obtained using `combfreqcount` in increasing order and assigns each symbol a weight corresponding to its rank.
- `invcombfreqcount` is `combfreqcount`'s inverse. Whenever `combfreqcount` would assign a weight  $w$  to a symbol, it assigns  $M - w + 1$ , where  $M$  is the maximum symbol weight according to `combfreqcount`.
- `invcombfreqrank` is `combfreqrank`'s inverse. It sorts the frequencies in decreasing order.

To generate symbol precedences, E can sort symbols by weight and use the symbol's position in the sorted array as the basis for precedence. To account for the type information introduced by the applicative encoding, we implemented four type-aware precedence generation schemes, called `typefreq`, `invtypefreq`, `combfreq`, and `invcombfreq`, that sort the symbols by weight according to `typefreqcount`, `invtypefreqcount`, `combfreqcount`, and `invcombfreqcount`, respectively. Ties are broken by comparing the symbols' number of occurrences and, if necessary, the position of their first occurrence in the input problem.

## 7.2 Literal Selection

The side conditions of the superposition rules (SN and SP, Sect. 6.1) allow the use of a literal selection function to restrict the set of *inference literals*, thereby pruning the search space. Given a clause, a literal selection function returns a (possibly empty) subset of its literals. For completeness, any nonempty subset selected must contain at least one negative literal. If no literal is selected, all *maximal* literals become inference literals. The most widely used function in E is probably `SelectMaxLComplexAvoidPosPred`, which we abbreviate to `SelectMLCAPP`. It selects at most one negative literal, based on size, groundness, and maximality of the literal in the clause. It also avoids negative literals that share a predicate symbol with a positive literal in the same clause.

Intuitively, applied variables can potentially be unified with more terms than terms with rigid heads. This makes them prolific in terms of possible inference partners, a behavior we might want to avoid. However, shorter proofs might be found if we prefer selecting applied variables. To cover both scenarios, we implemented selection functions that prefer or defer selecting applied variables.

Let  $\text{max}(L) = 1$  if  $L$  is a maximal literal of the clause it appears in; otherwise,  $\text{max}(L) = 0$ . Let  $\text{appvar}(L) = 1$  if  $L$  is a literal where either side is an applied variable; otherwise,  $\text{appvar}(L) = 0$ . Based on these definitions, we devised the following selection functions, both of which rely on `SelectMLCAPP` to break ties:

- `SelectMLCAPPAvoidAppVar` selects a negative literal  $L$  with the maximal value of  $(\text{max}(L), 1 - \text{appvar}(L))$  according to the lexicographic order.
- `SelectMLCAPPPreferAppVar` selects a negative literal  $L$  with the maximal value of  $(\text{max}(L), \text{appvar}(L))$  according to the lexicographic order.

The presence of  $\text{max}(L)$  as the first criterion is motivated by initial experiments.

### 7.3 Clause Selection

Selection of the given clause is a critical choice point. E heuristically assigns *clause priorities* and *clause weights* to the candidates. The priorities provide a crude partition, whereas the weights are used to further distinguish candidates. E’s main loop visits, in round-robin fashion, a set of priority queues. From each queue, it selects a number of clauses with the highest priorities, breaking ties by preferring smaller weights. Typically, one of the queues will use the clauses’ age as priority, to ensure fairness.

E provides template weight functions that allow users to fine-tune parameters such as weights assigned to variables or function symbols. The most widely used template is `ConjectureRelativeSymbolWeight`, which we abbreviate to `CRSWeight`. It computes term and clause weights according to the following parameters:

*conj\_mul* multiplier applied to the weight of conjecture symbols;  
*fweight* weight of a nonnullary function symbol;  
*cweight* weight of a nullary function symbol;  
*pweight* weight of a predicate symbol;  
*vweight* weight of a variable;  
*maxt\_mul* multiplier applied to the weight of maximal terms in a literal;  
*maxl\_mul* multiplier applied to the weight of maximal literals in a clause;  
*pos\_mul* multiplier applied to the weight of positive literals in a clause.

This templates works well for some applicatively encoded problems. To see why, let  $\mathbf{a} : \iota$ ,  $\mathbf{f} : \iota \rightarrow \iota$ ,  $x : \iota$ , and  $y : \iota \rightarrow \iota$ , and consider the clauses

$$y\ x \not\approx x \qquad \mathbf{f}\ \mathbf{a} \approx \mathbf{a}$$

where the first one is the negated conjecture. Their applicative encoding is

$$\text{@}_{\iota,\iota}(y, x) \not\approx x \qquad \text{@}_{\iota,\iota}(\mathbf{f}, \mathbf{a}) \approx \mathbf{a}$$

The encoded clauses share the symbol  $\text{@}_{\iota,\iota}$ , whose weight will be multiplied by *conj\_mul*—typically a factor in the interval  $(0, 1)$ . By contrast, the native  $\lambda$ FHOL

clauses share no symbols, and the heuristic would fail to notice that  $f$  and  $y$  have the same type, giving a higher weight to the second clause.

To mitigate this effect, we implemented a new type-aware template function, called `CRSTypeWeight`, that behaves like `CRSWeight` except that it applies the `conj_mul` multiplier to all symbols whose type occurs in the conjecture. For the example above, since  $\iota \rightarrow \iota$  appears in the conjecture, it would notice the relation between the conjecture variable  $y$  and the symbol  $f$  and multiply  $f$ 's weight by `conj_mul`.

A benefit of natively supporting  $\lambda$ HOL is that the prover can recognize applied variables. It may make sense to penalize clauses that depend on this higher-order feature—or perhaps such clauses should be promoted instead. To exploit this kind of information in heuristics, we extended most of E's weight function templates, as well as `CRSTypeWeight`, with the following optional parameter:

`appv_mul` multiplier applied to terms that constitute either side of a literal and whose head is a variable applied to at least one argument.

In addition, we implemented a new clause priority scheme, `ByAppVarNum`, that separates the clauses by the number of top-level applied variables occurring in the clause, favoring those containing fewer such variables.

## 7.4 Configurations and Modes

A combination of proof search parameters—including term order, literal selection, and clause selection—is called a *configuration*. It is generally acknowledged that most provable problems arising in practice can be proved quickly in at least one configuration; but the configuration space being infinite, there is no easy way to find this configuration.

For years, E has provided an *auto* mode, which analyzes the input problem and chooses a configuration known to perform well on similar problems. More recently, E has been extended with an *autoschedule* mode, which applies a portfolio of configurations in sequence on the given problem, restarting the prover for each configuration. Time slicing approaches tend to perform better in practice, even if each configuration is given a shorter time slice.

Configurations that perform well on a wide range of problems have emerged over time. One of them is the configuration that is most often chosen by E's *auto* mode. We call it *boa* (“best of *auto*”):

```

Term order:      KBO
Weight generation:  invfreqrank
Precedence generation: invfreq
Literal selection: SelectMLCAPP
Clause selection:  1.CRSWeight(SimulateSOS,
                   0.5, 100, 100, 100, 100, 1.5, 1.5, 1),
                   4.CRSWeight(ConstPrio,
                   0.1, 100, 100, 100, 100, 1.5, 1.5, 1.5),
                   1.FIFOWeight(PreferProcessed),

```

```

1.CRSWeight(PreferNonGoals,
             0.5, 100, 100, 100, 100, 1.5, 1.5, 1),
4.Refinedweight(SimulateSOS, 3, 2, 2, 1.5, 2)

```

The clause selection scheme consists of five queues, each of which is specified by a weight function template. The prefixes  $n.$  next to the template names indicate that  $n$  clauses should be taken from the corresponding queue each time it is visited. The first argument to each template is the clause priority scheme.

## 8 Preprocessing

E includes a preprocessor that transforms first-order formulas into clausal normal form, before the main loop is started and inference rules are applied. Beyond turning the problem into a conjunction of disjunctive clauses, the preprocessor eliminates quantifiers, introducing Skolem symbols for essentially existential quantifiers. For first-order logic, skolemization preserves satisfiability (unprovability) and unsatisfiability (provability). In contrast, for higher-order logics without the axiom of choice, naive skolemization is unsound, because it introduces symbols that can be used to instantiate higher-order variables.

One solution proposed by Miller [33, Sect. 6] is to ensure that Skolem symbols are always applied to a minimum number of arguments. Accordingly, the version of  $\lambda$ FHOL introduced by Bentkamp et al. [8] distinguishes between mandatory and optional arguments. However, to keep the implementation simple, we have decided to ignore this issue and consider all arguments as optional, including those to Skolem symbols. We expect to extend Ehoh’s logic to full higher-order logic with the axiom of choice, which will address the issue.

There is another transformation performed by preprocessing that is problematic, but for a different reason. *Definition unfolding* is the process of replacing equationally defined symbols with their definitions and removing the defining equations. A definition is a clause of the form  $f \bar{x}_m \approx t$ , where the variables  $\bar{x}_m$  are distinct,  $f$  does not occur in the right-hand side  $t$ , and  $\mathcal{V}ar(t) \subseteq \{x_1, \dots, x_m\}$ . This transformation preserves unsatisfiability (provability) for first- and higher-order logic, but not for  $\lambda$ FHOL, making Ehoh incomplete. The reason is that by removing the definitional clause, we also remove a symbol  $f$  that otherwise could be used to instantiate a higher-order quantifier. For example, the clause set  $\{f x \approx x, f (y a) \not\approx a\}$  is unsatisfiable, whereas  $\{y a \not\approx a\}$  is satisfiable in  $\lambda$ FHOL. (In full higher-order logic, the second clause set would be unsatisfiable thanks to the  $\{x \mapsto \lambda x. x\}$  instance and the  $\beta$ -rule.) For the moment, we have simply disabled definition unfolding in Ehoh. We will enable it again once we have added support for  $\lambda$ -terms.

## 9 Evaluation

In this section, we consider the following questions: How useful are Ehoh’s new heuristics? And how does Ehoh perform compared with the previous version of E, 2.2, used directly or in conjunction with the applicative encoding, and

compared with other provers? To answer the first question, we evaluated each new parameter independently. From the empirical results, we derived a new configuration optimized for  $\lambda$ FHOL problems. To answer the second question, we compared Ehoh’s success rate and speed on  $\lambda$ FHOL problems with native higher-order provers and with E’s on their applicatively encoded counterparts. We also included first-order benchmarks to measure Ehoh’s overhead with respect to E.

We set a CPU time limit of 60 s per problem. This is more than allotted by interactive proof tools such as Sledgehammer, or by cooperative provers such as Leo-III and Satallax, but less than the 300 s of CASC [49]. The experiments were performed on StarExec [45] nodes equipped with Intel Xeon E5-2609 0 CPUs clocked at 2.40 GHz and with 8192 MB of memory. Our raw experimental data are publicly available.<sup>2</sup>

## 9.1 Heuristics Tuning

We used the *boa* configuration as the basis to evaluate the new heuristic schemes. For each heuristic parameter we tuned, we changed only its value while keeping the other parameters the same as for *boa*. This gives an idea of how each parameter value affects overall performance. All heuristic parameters were tested on a 5012 problem suite generated using Sledgehammer, consisting of four versions of the Judgment Day [15] suite. The problems were given in native  $\lambda$ FHOL syntax.

**Term Order Generation.** Evaluating new weight and precedence generation heuristics amounts to testing each possible combination of frequency based schemes, including E’s original type-agnostic schemes. Figure 1 shows the number of solved (proved or disproved) problems for each combination. In this and the next figures, the slanted number corresponds to *boa*, whereas bold singles out the best value.

Figure 1 indicates that including type information in the generation schemes results in a somewhat higher number of solved problems compared with E’s type-agnostic schemes. Against our expectations, Ehoh’s combined schemes are generally less efficient than its type-aware schemes.

**Literal Selection.** The literal selection function has little impact on performance: Ehoh solves 2379 problems with `SelectMLCAPP` or `SelectMLCAPPAvoidAppVar`, and 2378 problems with `SelectMLCAPPPreferAppVar`.

**Clause Selection.** Clause selection is the heuristic component that we extended the most. We must assess the effect of a new heuristic weight function, a multiplier for the occurrence of top-level applied variables, and clause priority based on the number of top-level applied variables.

To test the effect of the new type-based heuristic weight function, we changed *boa*’s clause selection heuristic by using `4.CRSTypeWeight(...)` where *boa* specifies

<sup>2</sup> [http://matryoshka.gforge.inria.fr/pubs/ehoh\\_results.tar.gz](http://matryoshka.gforge.inria.fr/pubs/ehoh_results.tar.gz)



	freq	invfreq	typefreq	invtypefreq	combfreq	invcombfreq
freqcount	2294	2288	2287	2297	2290	2287
invfreqcount	2371	2373	2374	2370	2369	2377
freqrank	2326	2317	2323	2329	2322	2318
invfreqrank	2383	2379	2376	2380	2381	2381
typefreqcount	2305	2314	2301	2306	2302	2311
invtypefreqcount	2386	2381	2389	2388	2384	2379
typefreqrank	2326	2334	2322	2334	2321	2336
invtypefreqrank	2390	2382	2390	<b>2394</b>	2387	2386
combfreqcount	2273	2281	2271	2285	2269	2280
invcombfreqcount	2380	2375	2382	2379	2380	2375
combfreqrank	2321	2313	2319	2321	2318	2312
invcombfreqrank	2368	2378	2371	2378	2368	2380

Fig. 1. Evaluation of weight and precedence generation schemes

	0.25	0.35	0.5	0.7	1	1.41	2	2.82	4
CRSWeight	2311	2341	2363	2374	<b>2379</b>	2376	2377	2376	2377
CRSTypeWeight	2331	2331	2360	2371	2372	<b>2374</b>	2373	2373	2372

Fig. 2. Evaluation of weight function and applied variable multipliers

4. `CRSWeight(...)`. We chose nine values between 0.25 and 4 for testing the effect of the applied variable multiplier.

Figure 2 summarizes the results of combining weight functions `CRSWeight` and `CRSTypeWeight` with the different values for the applied variable multiplier. Applying a multiplier smaller than 1, which corresponds to preferring literals containing applied variables, can result in losing dozens of solutions. Overall, it would seem that using the type-aware heuristic is slightly detrimental.

Finally, we evaluated the new clause priority `ByAppVarNum`, based on the number of top-level applied variables, by replacing `4.CRSWeight(ConstPrio,...)` with `4.CRSWeight(ByAppVarNum,...)` in *boa*'s specification. `ConstPrio`, which assigns each clause the same priority, enabled `Ehoh` to solve 2379 problems. By contrast, `ByAppVarNum` led to 2377 solved problems. These results suggest that `ByAppVarNum` is not particularly helpful.

**New Configuration.** The results presented above give an overview of how each parameter influences performance. We also evaluated their performance in combination, to derive an alternative to *boa* for  $\lambda$ FHOL.

For each category of parameters, we chose either *boa*'s value of the parameter in *boa* (“old”) or the best performing newly implemented parameter (“new”). Based on the results above, for term orders, we chose the combination of `invtypefreqrank` and `invtypefreq`; for clause selection, we chose `CRSTypeWeight` with `ConstPrio` priority and an *appv\_mult* factor of 1.41; for literal selection, we chose `SelectMLC-APPAvoidAppVar`.

Figure 3 shows the number of solved problems for all combinations of these parameters. From the two configurations that solve 2397 problems, we selected

Term order	Literal selection	Clause weight	Solved
Old	Old	Old	2379
Old	Old	New	2374
Old	New	Old	2379
Old	New	New	2373
New	Old	Old	2394
New	Old	New	<b>2397</b>
New	New	Old	2395
New	New	New	<b>2397</b>

**Fig. 3.** Evaluation of combinations of new parameters

the “New Old New” combination as our suggested “higher-order best of *auto*,” or *hoboa*, configuration. In the next subsection, we present a more detailed evaluation of *hoboa*, along with other configurations, on a larger benchmark suite.

## 9.2 Main Evaluation

The benchmarks are partitioned as follows: (1) 1147 first-order TPTP [47] problems belonging to the FOF (untyped) and TF0 (monomorphic) categories, excluding arithmetic; (2) 5012 Sledgehammer-generated problems from the Judgment Day [15] suite, targeting the monomorphic first-order logic embodied by TPTP TF0; (3) all 530 monomorphic higher-order problems from the TH0 category of the TPTP library belonging to the  $\lambda$ HOL fragment; (4) 5012 Judgment Day problems targeting the  $\lambda$ HOL fragment of TPTP TH0.

The TPTP library includes benchmarks from various fields of computer science and mathematics. It is the de facto standard for evaluating and testing automatic provers, but it has few higher-order problems. For the first group of benchmarks, we randomly chose 1000 FOF problems (out of 8172) and all monomorphic TFF problems that are parsable by E within 60 s (amounting to 147 out of 231 monomorphic TFF problems). Both groups of Sledgehammer problems include two subgroups of 2506 problems, generated to include 32 or 512 Isabelle lemmas (SH32 and SH512), to represent both smaller and larger problems arising in interactive verification. Each subgroup itself consists of two sub-subgroups of 1253 problems, generated by using either  $\lambda$ -lifting or SK-style combinators to encode  $\lambda$ -expressions.

We evaluated Ehoh against two higher-order provers, Leo-III and Satallax, and a version of E, which we call @+E, that first performs the applicative encoding. Leo-III and Satallax have the advantage that they can instantiate higher-order variables by  $\lambda$ -terms. Thus, some formulas that are provable by these two systems may be nontheorems for @+E and Ehoh, or they may require tedious reasoning about  $\lambda$ -lifted functions or SK-style combinators. A simple example is the conjecture  $\exists f. \forall x y. f x y \approx g y x$ , whose proof requires taking  $\lambda x y. g y x$  as the witness for  $f$ .

We also evaluated E, @+E, Ehoh, and Leo-III on first-order benchmarks to measure the overhead introduced by our extensions, as well as that entailed by

	First-order			Higher-order		
	TPTP	SH32	SH512	TPTP	SH32	SH512
E a	598	939	1234			
E as	<b>645</b>	950	<b>1311</b>			
E b	546	944	1243			
@+E a	526	943	1114	395	962	1119
@+E as	567	950	1151	397	965	1155
@+E b	538	942	1228	397	960	1272
Ehoh a	599	938	1233	396	962	1240
Ehoh as	644	949	1310	395	<b>973</b>	<b>1325</b>
Ehoh b	547	944	1243	396	966	1244
Ehoh hb	502	944	1231	393	968	1262
Leo-III	542	<b>951</b>	1126	<b>421</b>	963	1145
Satallax				406	768	790

**Fig. 4.** Number of proved problems

the applicative encoding. (Satallax is not included because it can only parse THF problems.) The number of problems each system proved is given in Figure 4. We considered the E modes *auto* (a) and *autoschedule* (as) and the configurations *boa* (b) and *hoboa* (hb). We observe the following:

- Comparing the Ehoh rows with the corresponding E rows, we see that Ehoh’s overhead is barely noticeable—the difference is at most one problem.
- Ehoh generally outperforms the applicative encoding, on both first-order and higher-order problems. On Sledgehammer benchmarks, the best Ehoh mode (*autoschedule*) clearly outperforms all @+E modes and configurations. Despite this, there are problems that @+E proves faster than Ehoh, because the applicative encoding impacts the heuristics in subtle ways.
- Especially on large benchmarks, the E variants are substantially more successful than Leo-III and Satallax. This corroborates older experiments by Sultana, Blanchette, and Paulson [46] involving LEO-II and Satallax. On the other hand, Leo-III emerges as the winner on the first-order SH32 benchmark set, presumably thanks to the combination of first-order backends (CVC4, E, and iProver) it depends on.
- The new *hoboa* configuration outperforms *boa* on higher-order problems, suggesting that it could be worthwhile to re-train *auto* and *autoschedule* based on  $\lambda$ HOL benchmarks and to design further heuristics.

Next to the number of problems proved, the time in which a prover gives an answer is also an important consideration. Figure 5 compares the average running times of systems on the problems that all of the applicable systems proved. The results show that Ehoh incurs little overhead on first-order problems. The raw evaluation data reveal that it takes Ehoh 3475 s to prove all these problems, compared with 3351 s for E, corresponding to a 3.7% overhead. We conjectured

	First-order			Higher-order		
	TPTP	SH32	SH512	TPTP	SH32	SH512
E a	0.42	0.21	0.66			
E as	0.45	0.31	1.20			
E b	0.60	<b>0.10</b>	<b>0.60</b>			
@+E a	<b>0.39</b>	0.15	0.62	0.06	0.10	0.39
@+E as	0.85	0.15	<b>0.60</b>	0.06	0.10	<b>0.37</b>
@+E b	0.51	0.14	1.00	0.06	0.09	0.72
Ehoh a	0.43	0.25	0.70	0.08	0.08	0.63
Ehoh as	0.46	0.31	1.20	0.23	0.17	1.12
Ehoh b	0.64	0.11	0.64	<b>0.05</b>	<b>0.07</b>	0.47
Ehoh hb	0.93	0.19	1.13	<b>0.05</b>	0.13	0.61
Leo-III	10.18	8.75	27.35	5.09	11.63	35.06
Satallax				2.92	5.87	9.95

**Fig. 5.** Average running times (s) on the problems proved by all provers

that the native treatment of  $\lambda$ HOL terms, which are roughly half the size of applicatively encoded terms, would result in a factor-of-2 speed-up of Ehoh over @+E, but this is not confirmed by the evaluation.

## 10 Discussion and Related Work

Our working hypothesis is that it should be possible to extend existing first-order provers to higher-order logic, without slowing them down unduly. Our research program is two-pronged: On the theoretical side, we are investigating higher-order extensions of superposition; on the practical side, we are implementing such extensions in a state-of-the-art prover. The work described in this report required modifying many parts of the E prover. The invariant that variables are unapplied and that symbols are always passed the same number of arguments were entrenched in E’s algorithms and data structures, requiring hundreds of modifications. Nonetheless, we found the generalization manageable and are now in a position to add support for  $\lambda$ -terms and higher-order unification.

Most higher-order provers were developed from the ground up. Two exceptions are Otter- $\lambda$  by Beeson [7] and Zipperposition by Cruanes [18]. Otter- $\lambda$  adds  $\lambda$ -terms and second-order unification to the superposition-based Otter [30]. The approach is pragmatic, with little emphasis on completeness. Zipperposition is a superposition-based prover written in OCaml. It was initially designed for first-order logic but subsequently extended to higher-order logic. Its performance is a far cry from E’s, but it is easier to modify. It competed at the 2017 edition of CASC [49] and is used by Bentkamp et al. [8] for experimenting with higher-order features. Finally, there is noteworthy preliminary work by the developers of Vampire [12] and of the SMT (satisfiability modulo theories) solvers CVC4 and veriT [5].

Native higher-order reasoning was pioneered by Robinson [37], Andrews [1], and Huet [23]. Andrews [2] and Benzmüller and Miller [10] provide excellent surveys. TPS, by Andrews et al. [3], was based on expansion proofs and let users specify proof outlines. The Leo family of systems, developed by Benzmüller and his colleagues, is based on resolution and paramodulation. LEO [9] featured support for extensionality on the calculus level and introduced the cooperative paradigm to integrate first-order provers. Leo-III [44] expands the cooperation with SMT (satisfiability modulo theories) solvers and introduces term orders in a pragmatic, incomplete way. Brown’s Satallax [16] is based on a complete higher-order tableau calculus, guided by a SAT solver; recent versions also cooperate with first-order provers. Satallax usually wins in CASC’s higher-order theorem division [49]. Another competitive prover is Lindblad’s AgsyHOL [27]. It is based on a focused sequent calculus driven by a generic narrowing engine.

An alternative to all of the above is to reduce higher-order logic to first-order logic by means of a translation. Robinson [38] outlined this approach decades before tools such as MizAR [50], Sledgehammer [34], HOLyHammer [24], and CoqHammer [19] popularized it in proof assistants. In addition to performing an applicative encoding, such translations must eliminate the  $\lambda$ -expressions [20, 32] and encode the type information [13]. In practice, on problems with a large first-order component, translations perform surprisingly well compared with the existing native provers [46]. Largely thanks to Sledgehammer, Isabelle often came in close second at CASC, even defeating Satallax in 2012 [48].

By removing the need for the applicative encoding, our work reduces the translation gap. The encoding buries the  $\lambda$ fHOL terms’ heads under layers of  $@$  symbols, which impacts the heuristics that inspect terms. Terms double in size, cluttering the data structures, and twice as many subterm positions must be considered for inferences. Moreover, encoding is incompatible with interpreted operators, notably for arithmetic. The traditional solution is to introduce proxies to connect an uninterpreted nullary symbol with its interpreted counterpart (e.g.,  $@(@(\text{plus}, x), y) \approx x + y$ ), but this is clumsy. A further complication is that in a monomorphic logic,  $@$  is not a single symbol but a type-indexed family of symbols  $@_{\tau, v}$ , which must be correctly introduced and recognized. Finally, the encoding must be undone in the generated proofs. While it should be possible to base a higher-order prover on such an encoding, the prospect is aesthetically and technically unappealing, and performance would likely suffer.

## 11 Conclusion

Despite considerable progress since the 1970s, higher-order automated reasoning has not yet assimilated some of the most successful methods for first-order logic with equality, such as superposition. We presented a graceful extension of a state-of-the-art first-order theorem prover to a fragment of higher-order logic devoid of  $\lambda$ -terms. Our work covers both theoretical and practical aspects. Experiments show promising results on  $\lambda$ -free higher-order problems and very little overhead for first-order problems, as we would expect from a graceful generalization.

The resulting Eho prover will form the basis of our work towards strong higher-order automation. Our aim is to turn it into a prover that excels on proof obligations emerging from interactive verification; in our experience, these tend to be large but only mildly higher-order. Our next steps will be to extend E’s term data structure with  $\lambda$ -expressions and investigate techniques for computing higher-order unifiers efficiently.

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