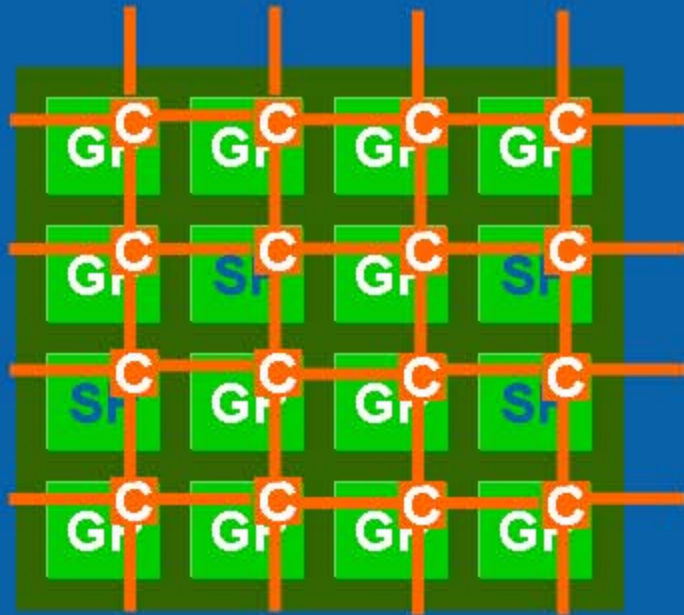


# Future Multi-core Platform



General Purpose Cores

Special Purpose HW

Interconnect fabric

**Heterogeneous Multi-Core Platform**